

FIG.1

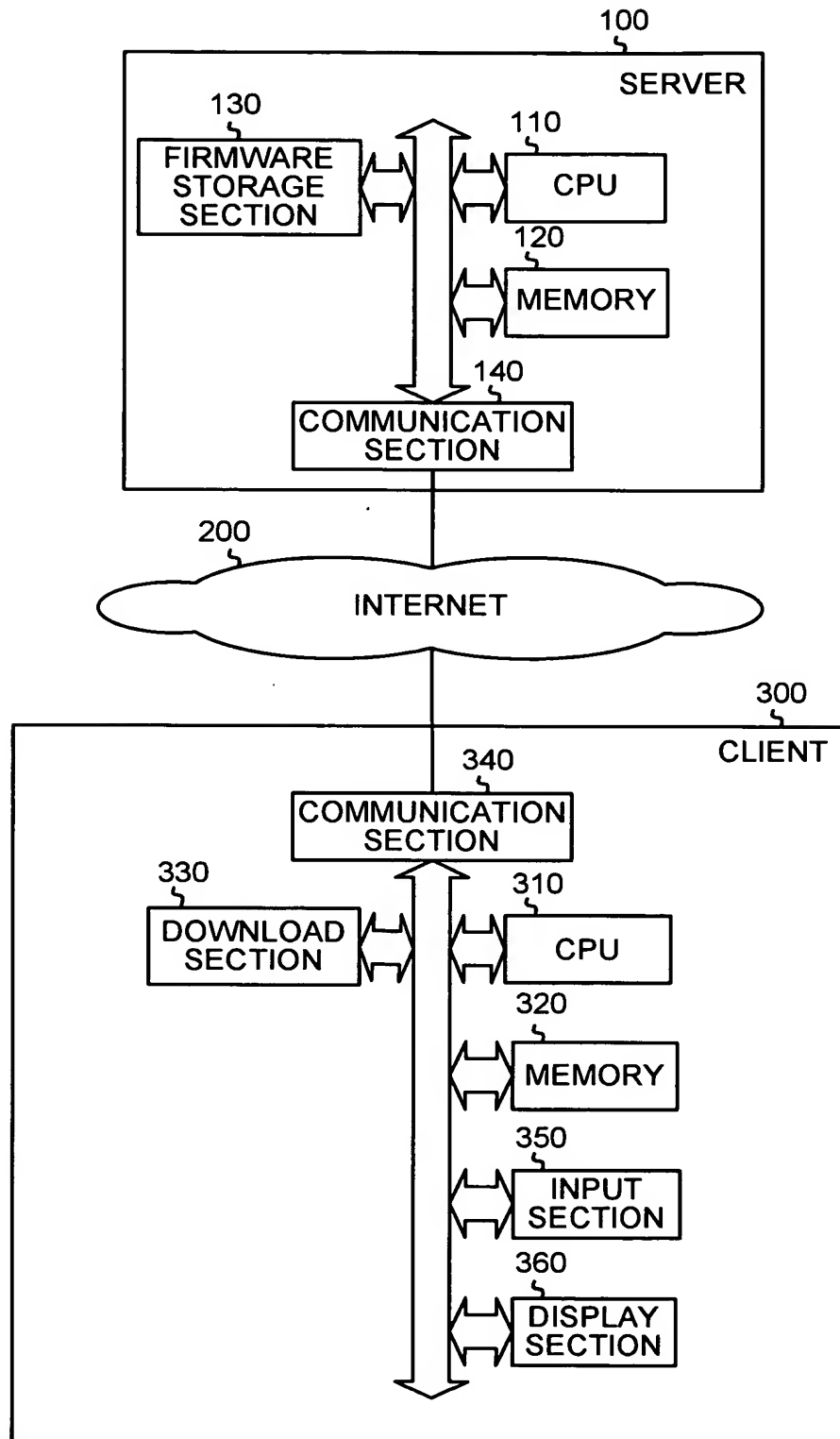


FIG.2

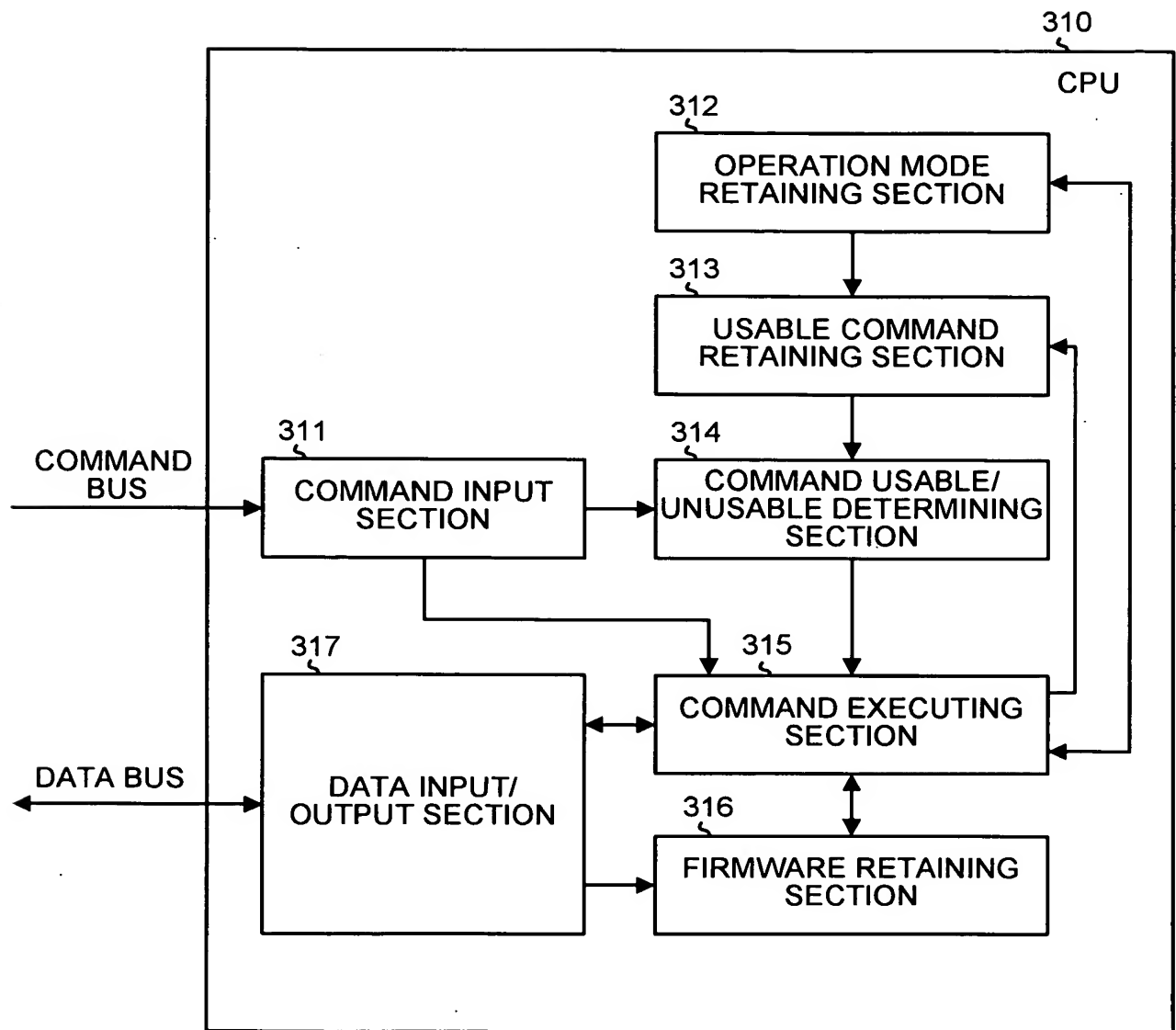


FIG.3

400
5

OPERATION MODE	NUMBER OF USABLE COMMANDS	COMMAND 1	COMMAND 2	COMMAND i	COMMAND n
0	n	0x01	0x13	0xf2	0xf8
1	i	0x11	0x23	0xe7	—	—
.
.
k	1	0xff	—	—	—	—	—

FIG.4

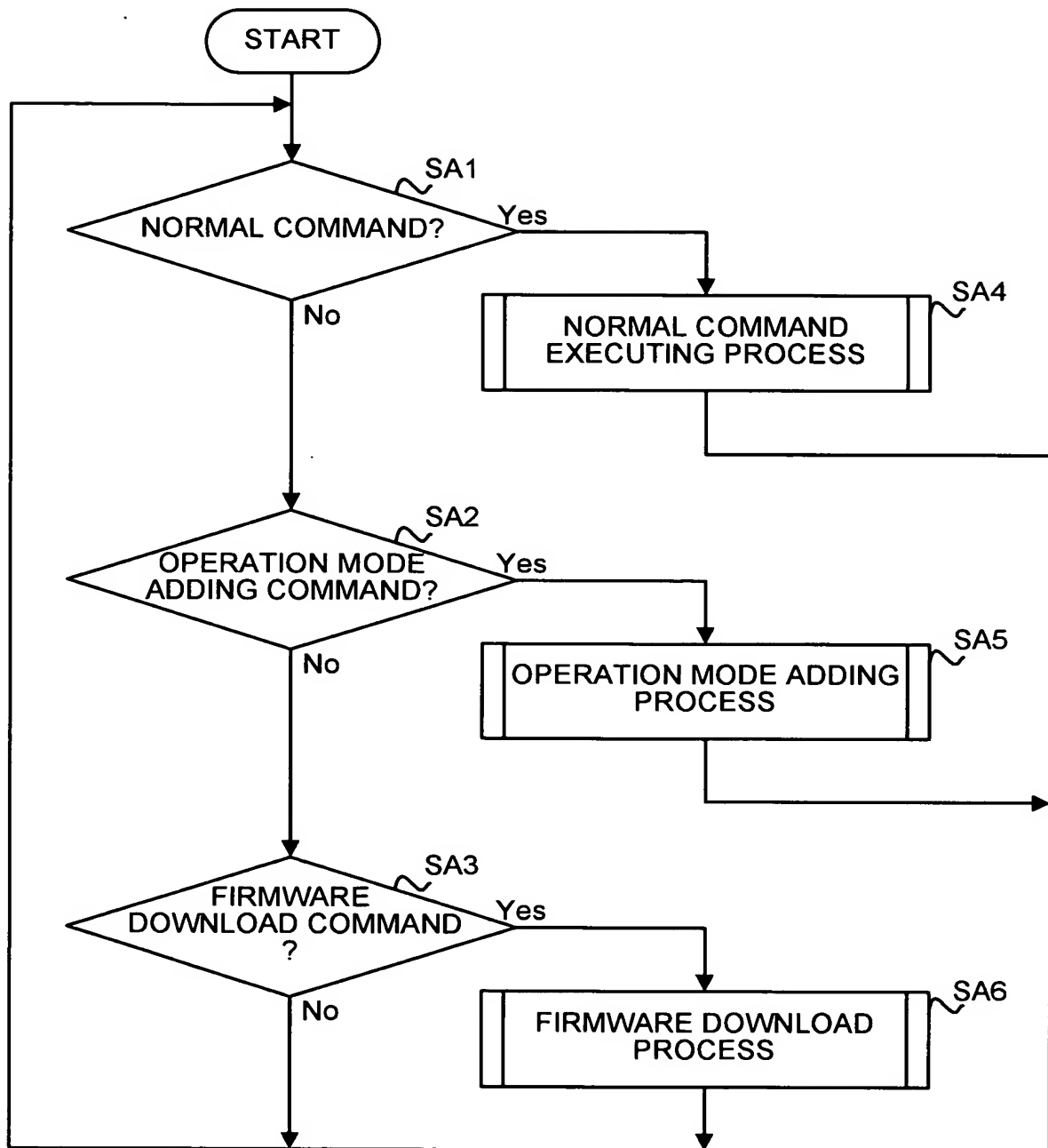


FIG.5

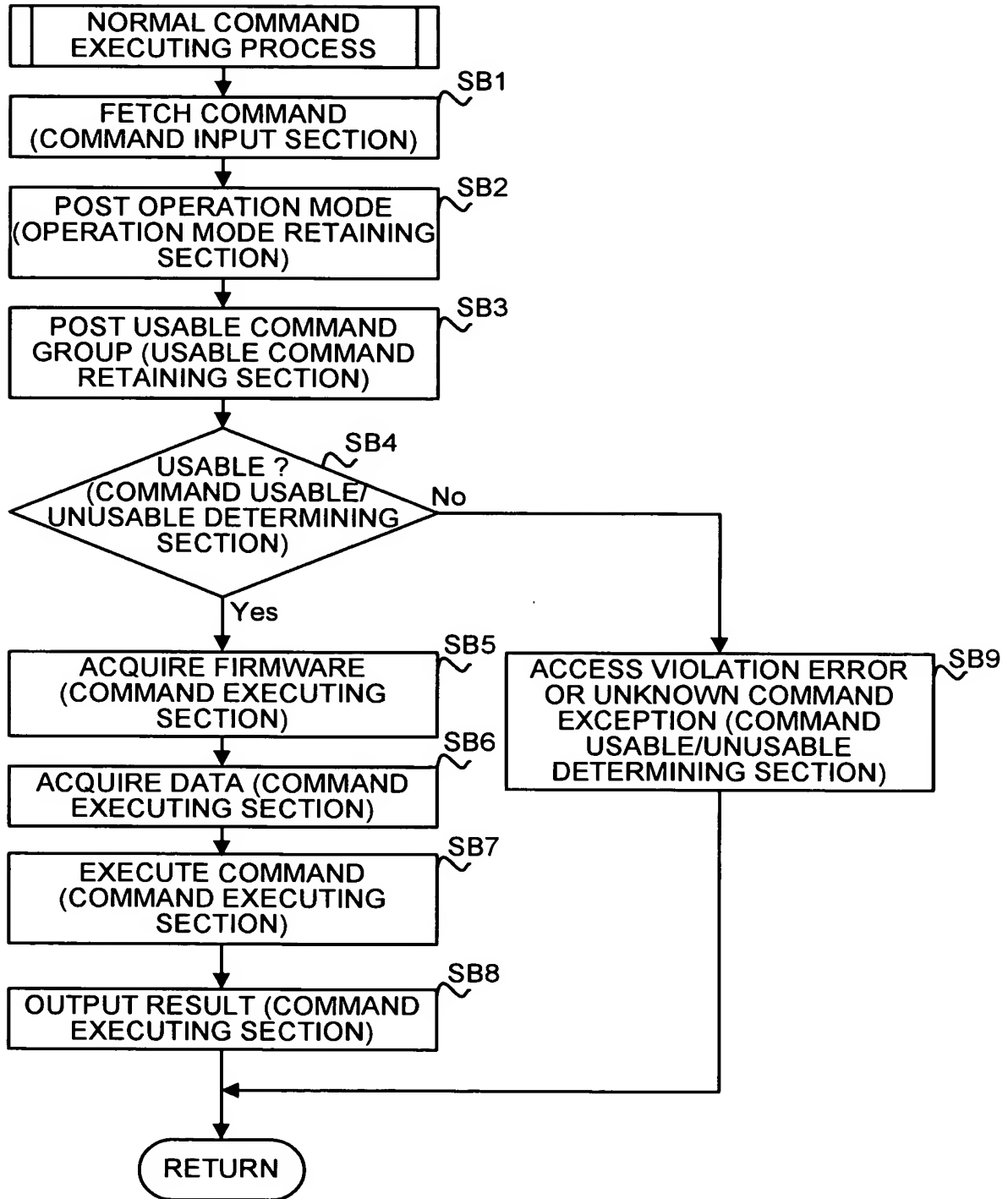


FIG.6

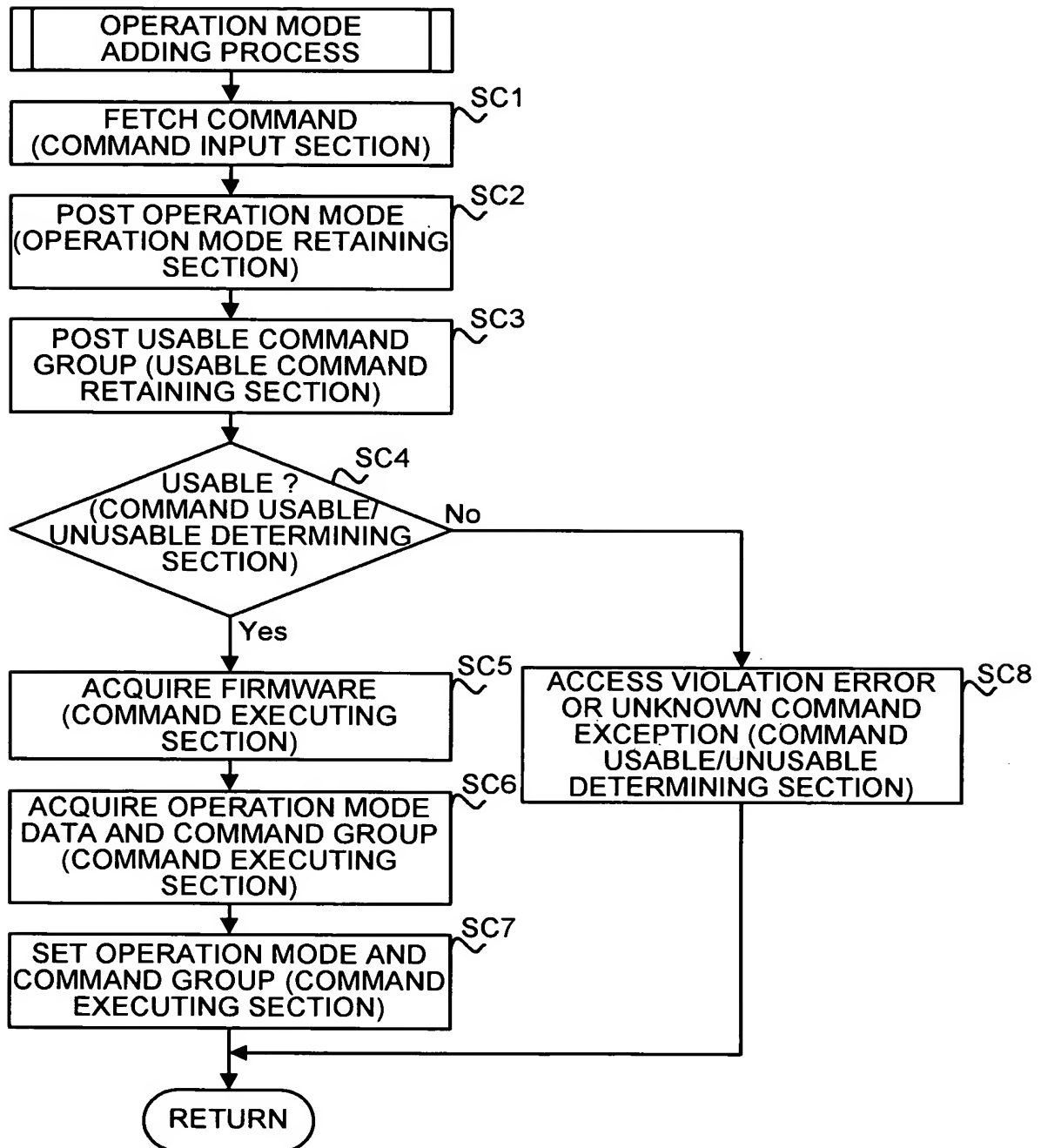


FIG.7

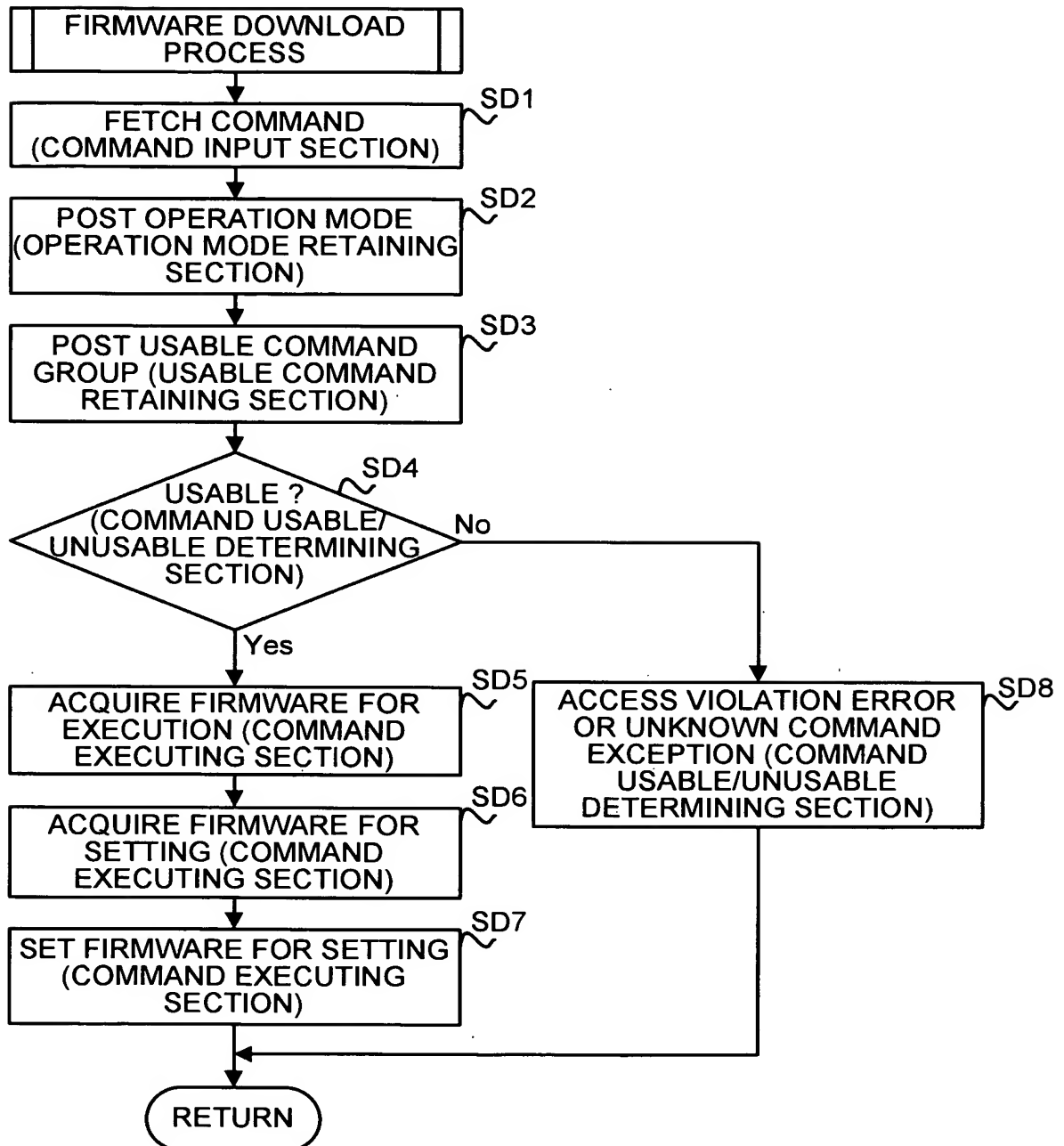


FIG.8

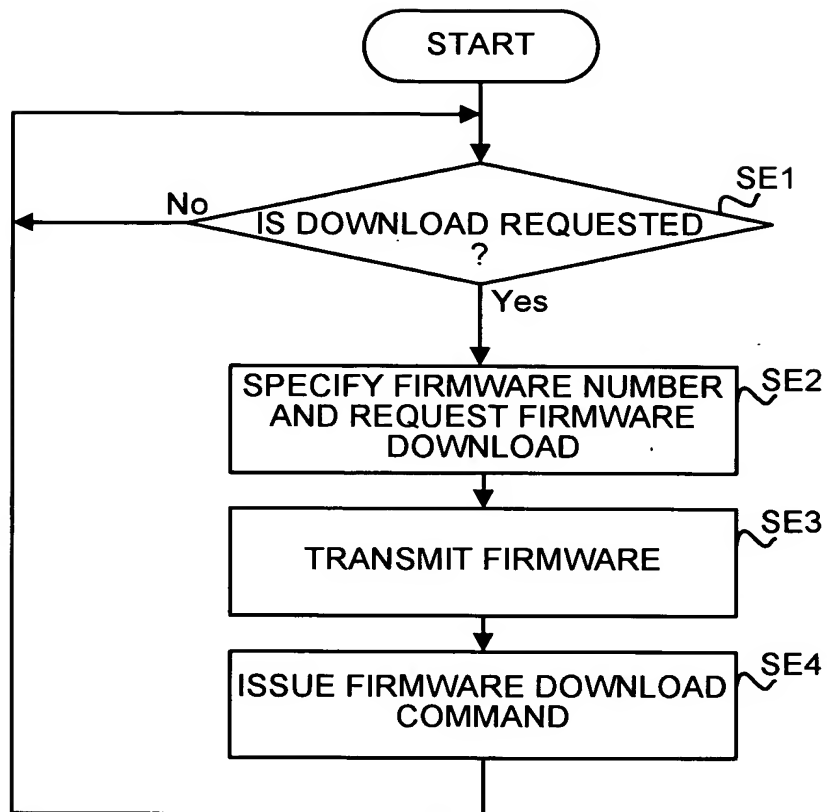


FIG.9

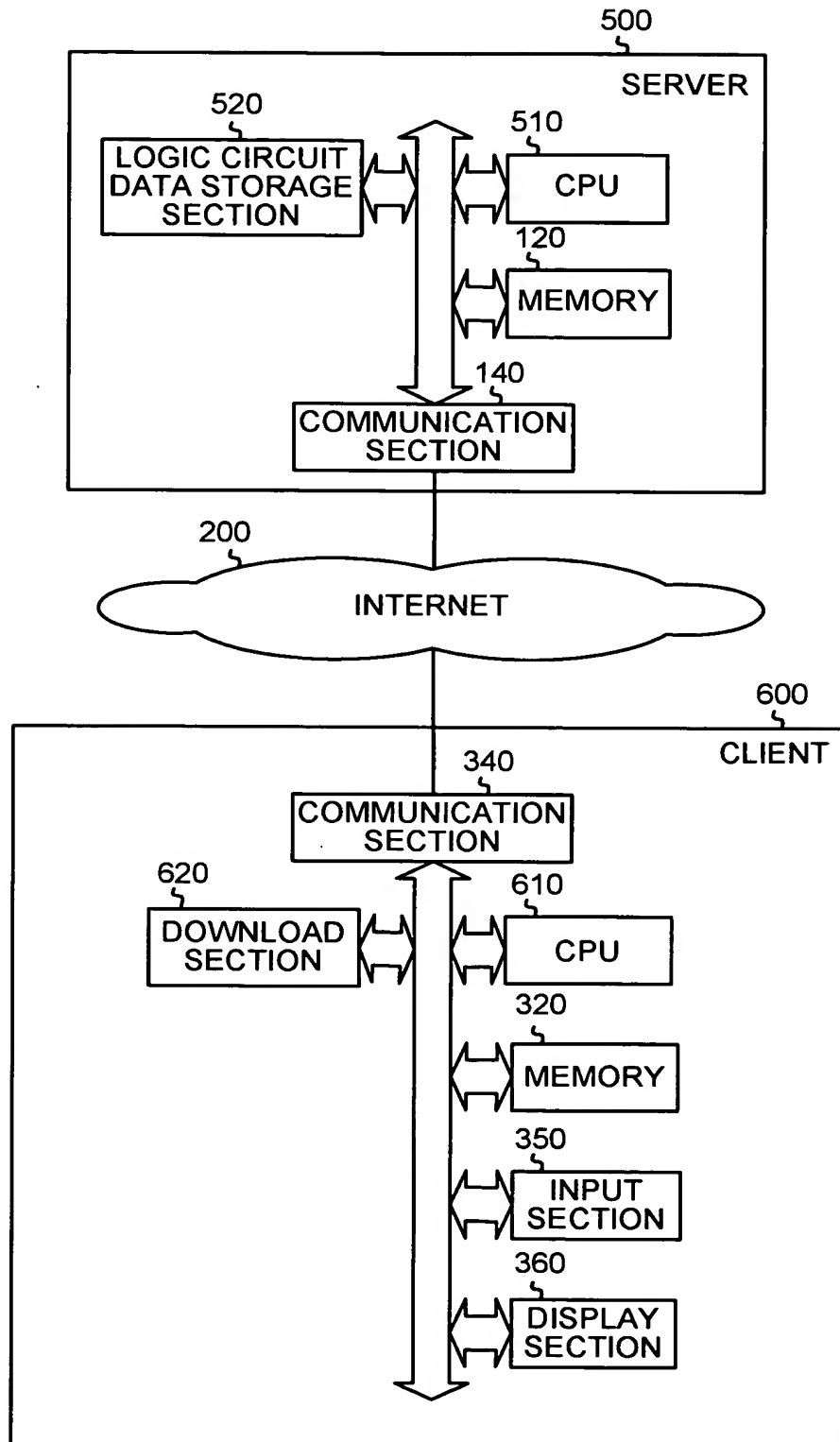


FIG.10

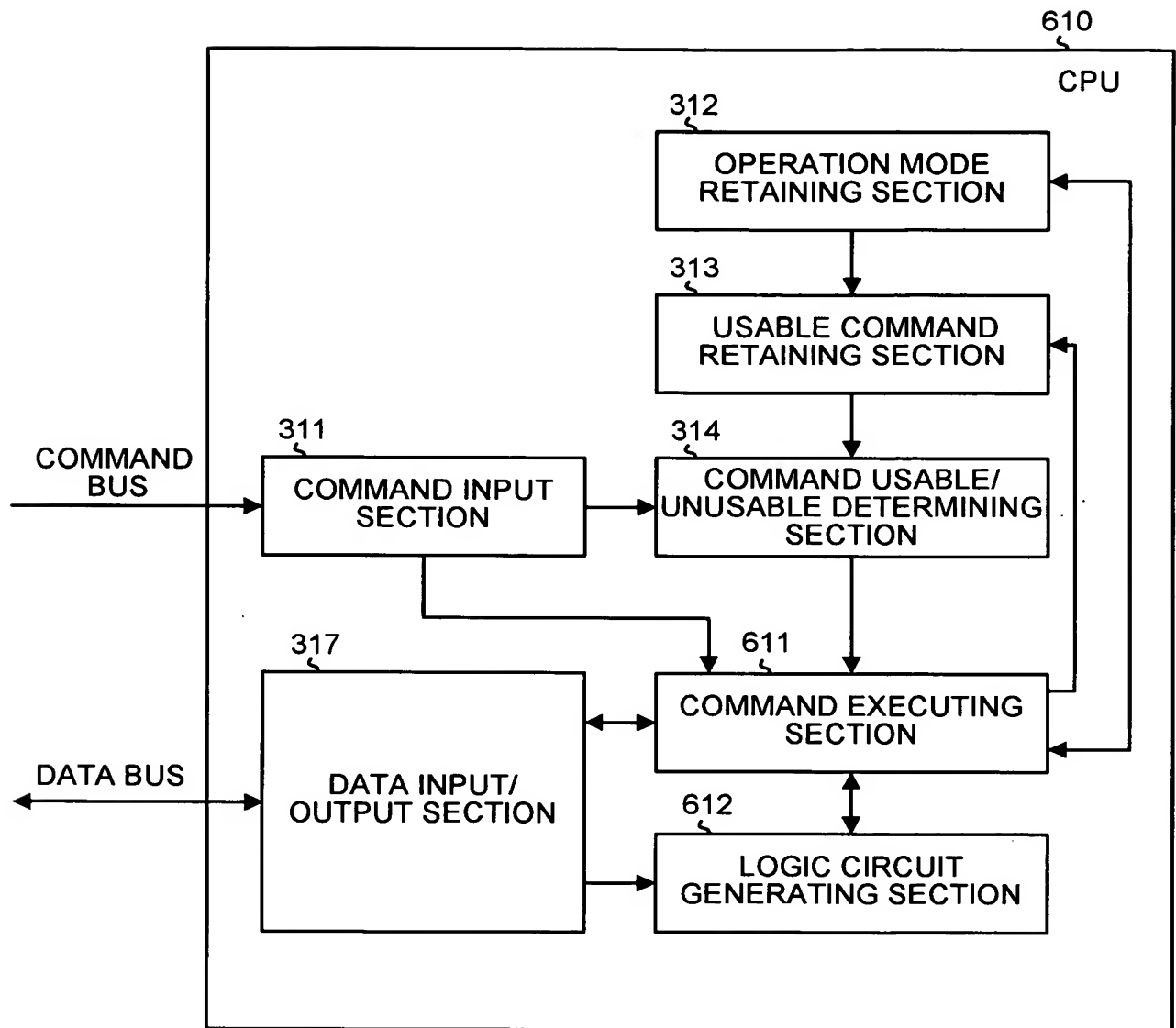


FIG.11

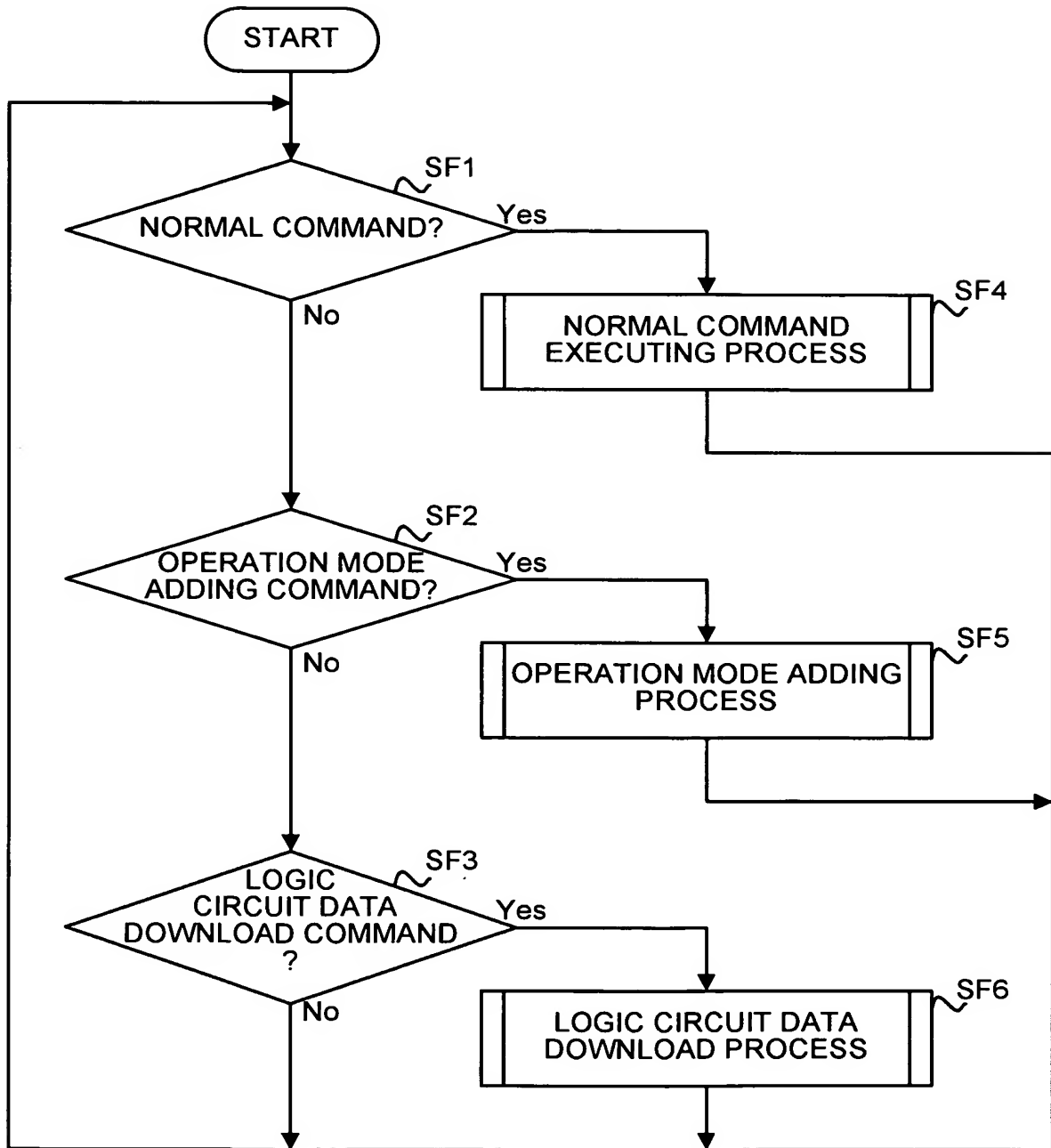


FIG.12

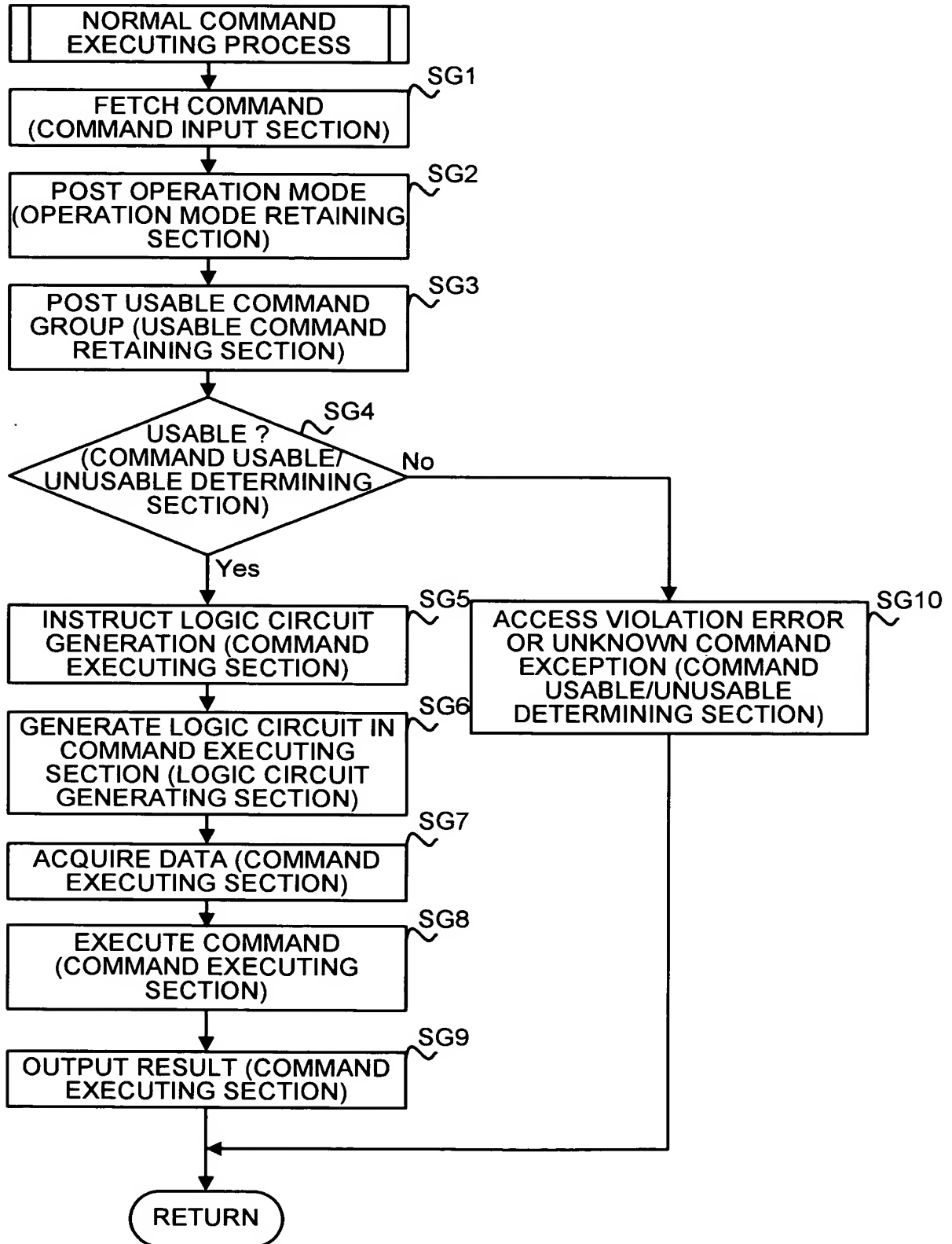


FIG.13

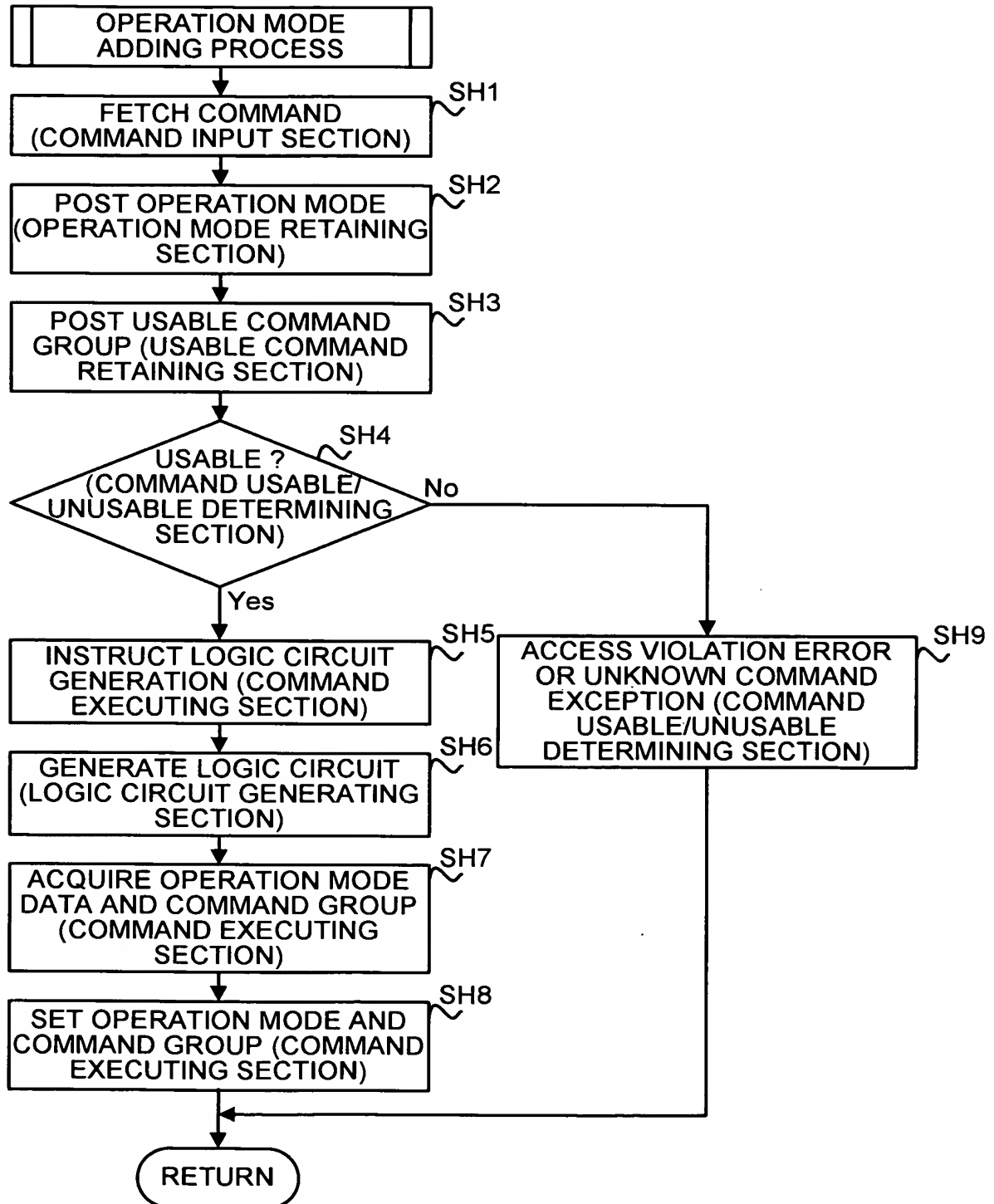


FIG.14

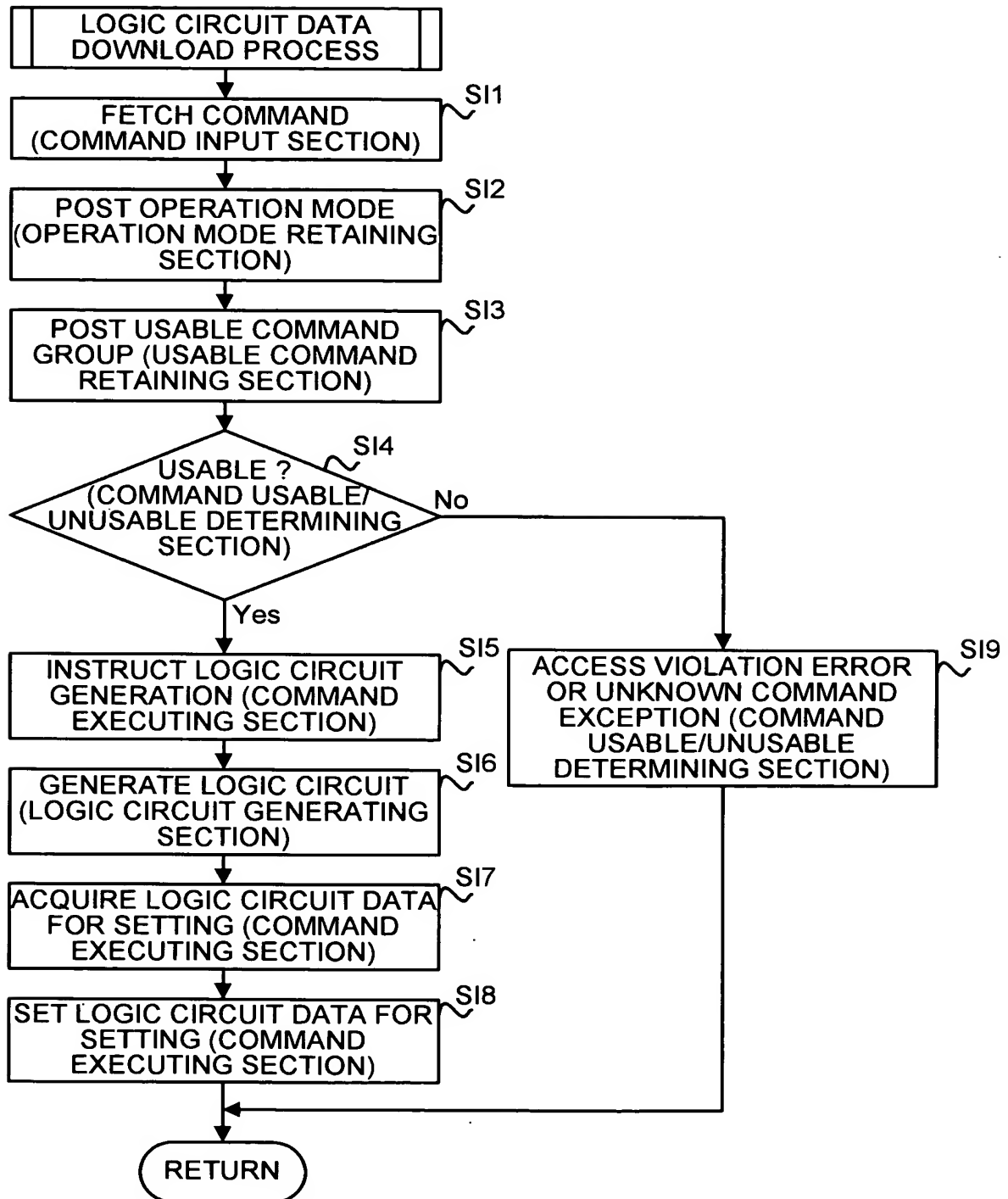


FIG.15

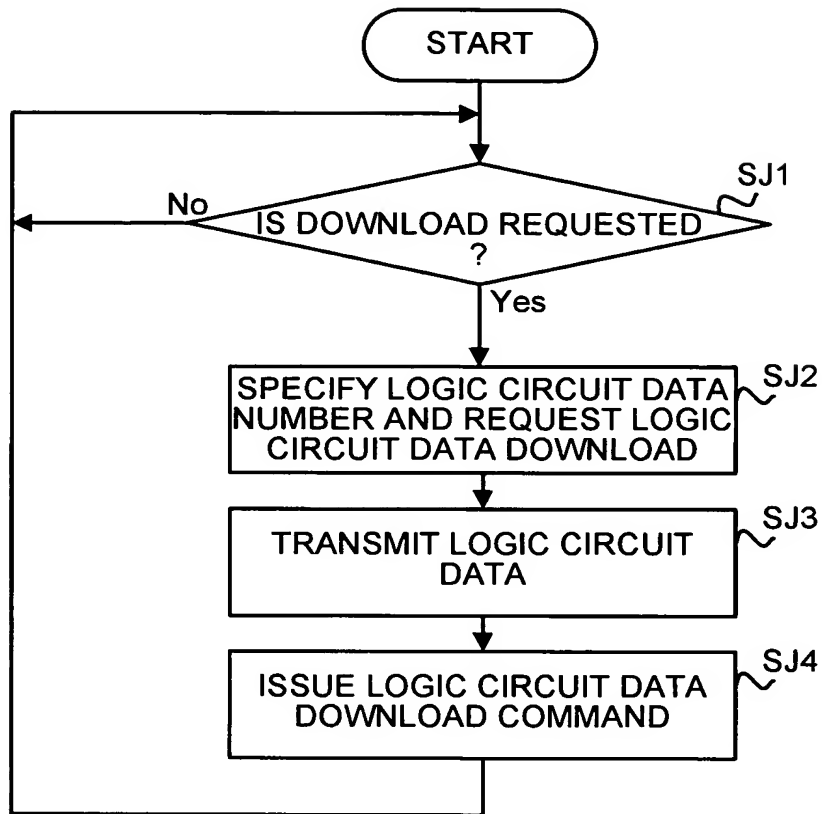


FIG. 16

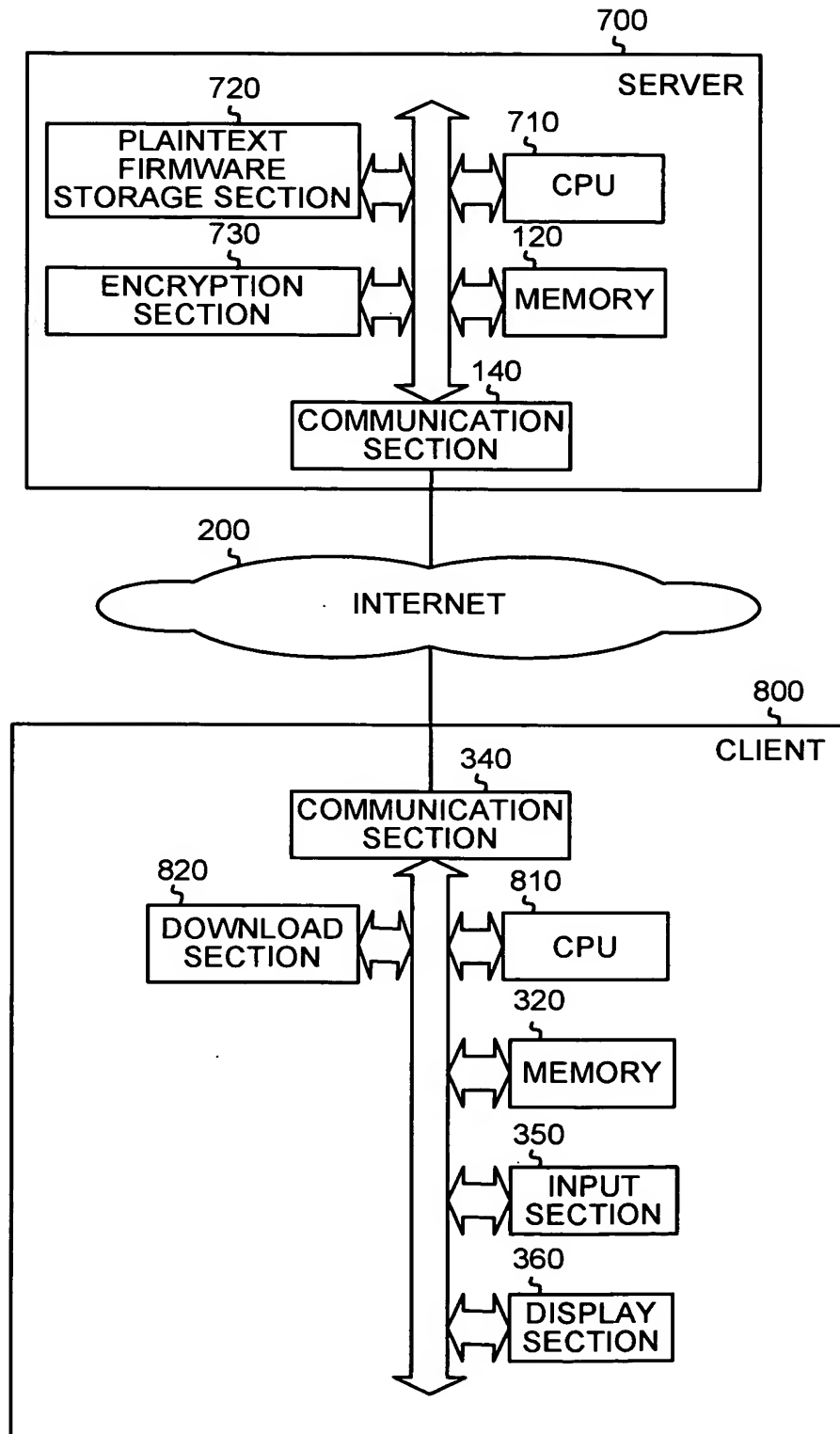


FIG.17

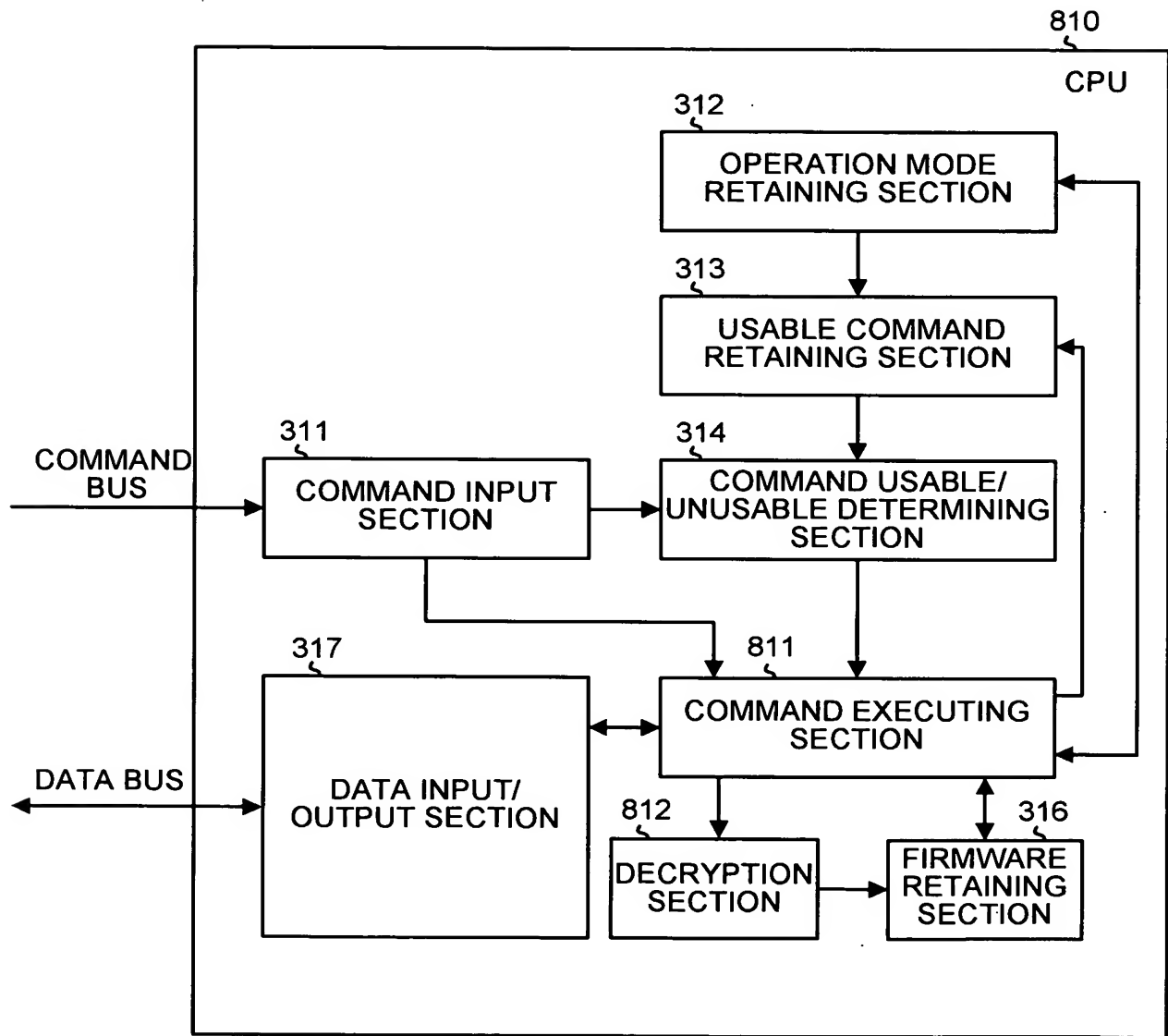


FIG.18

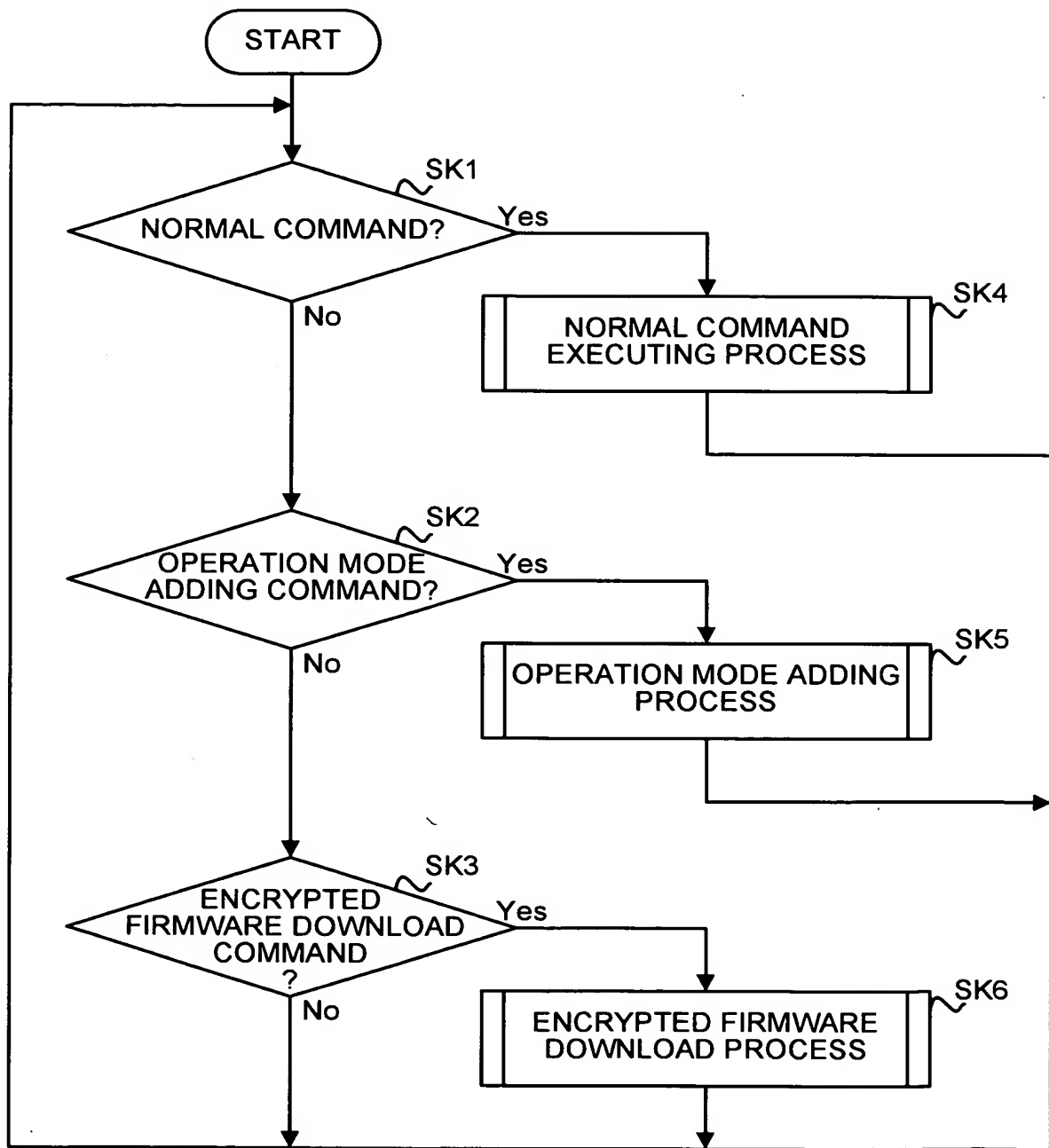


FIG.19

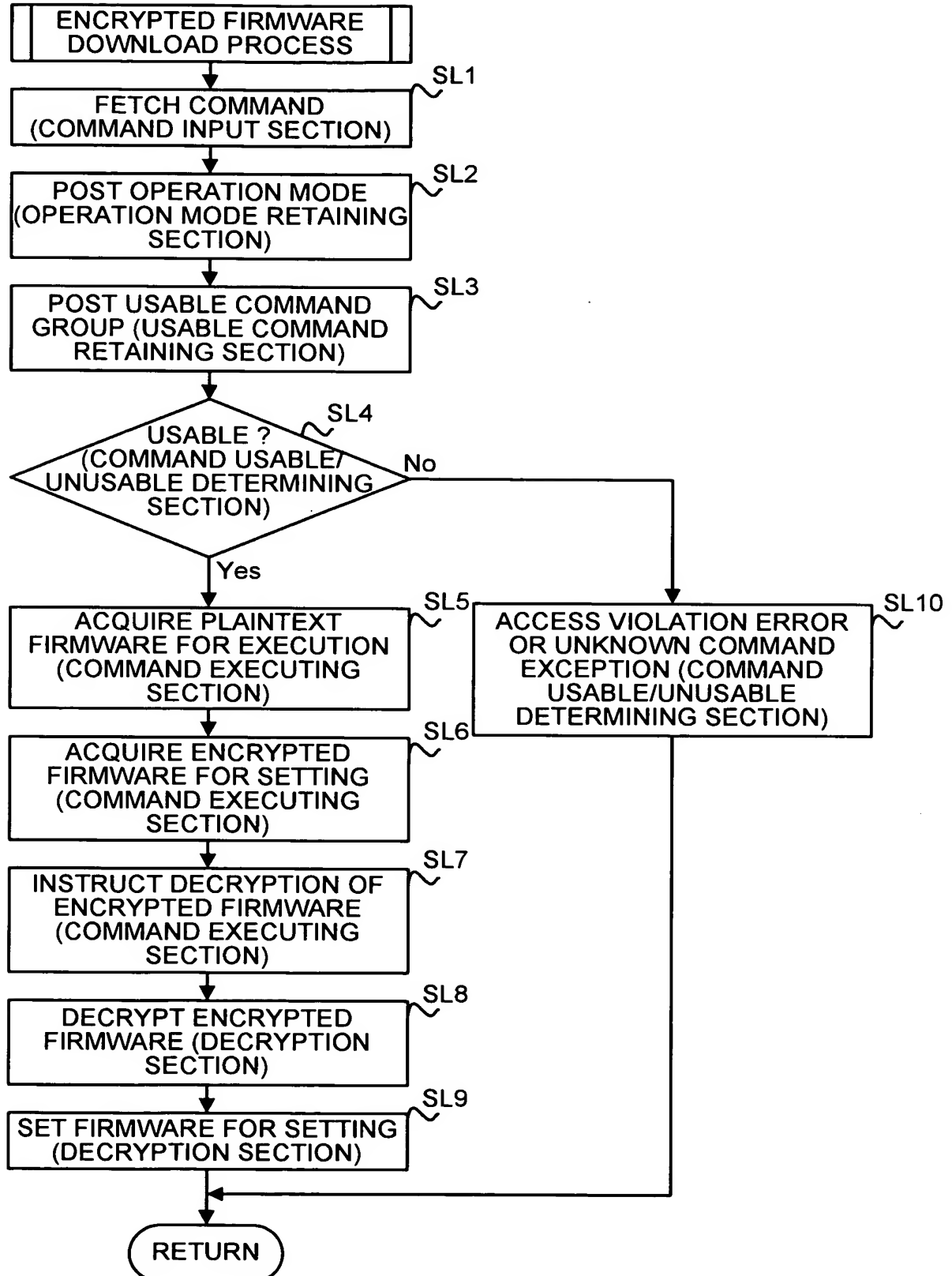


FIG.20

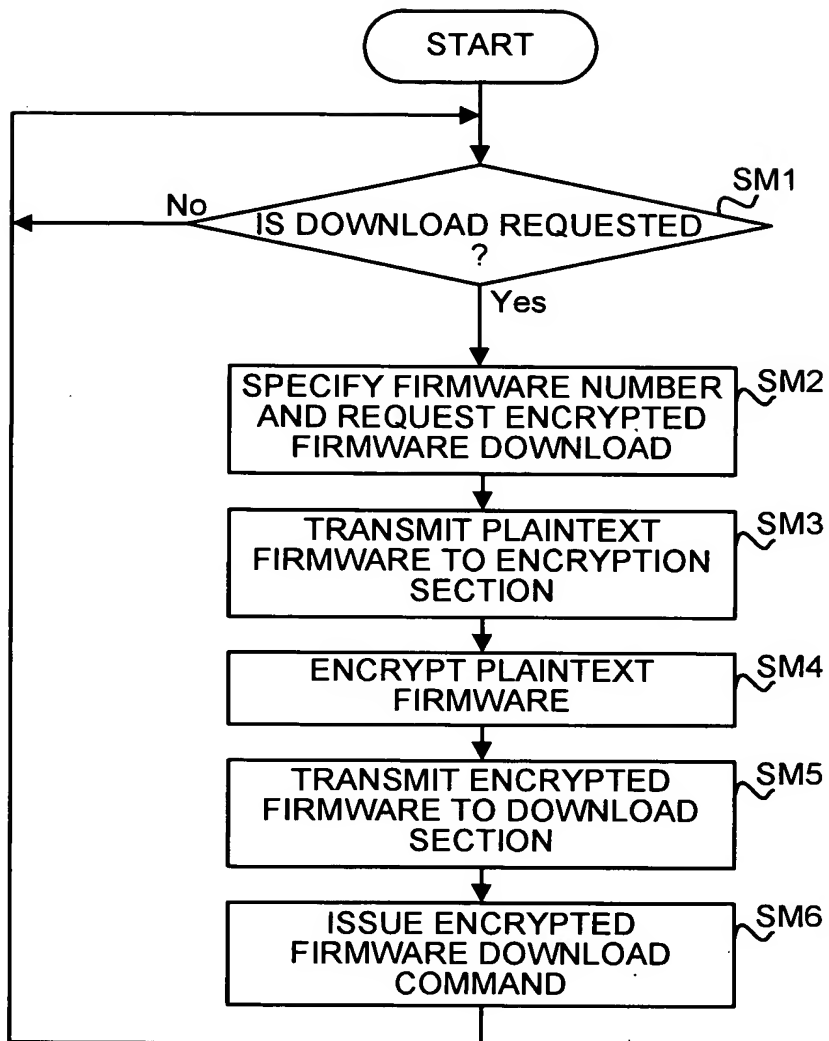


FIG.21

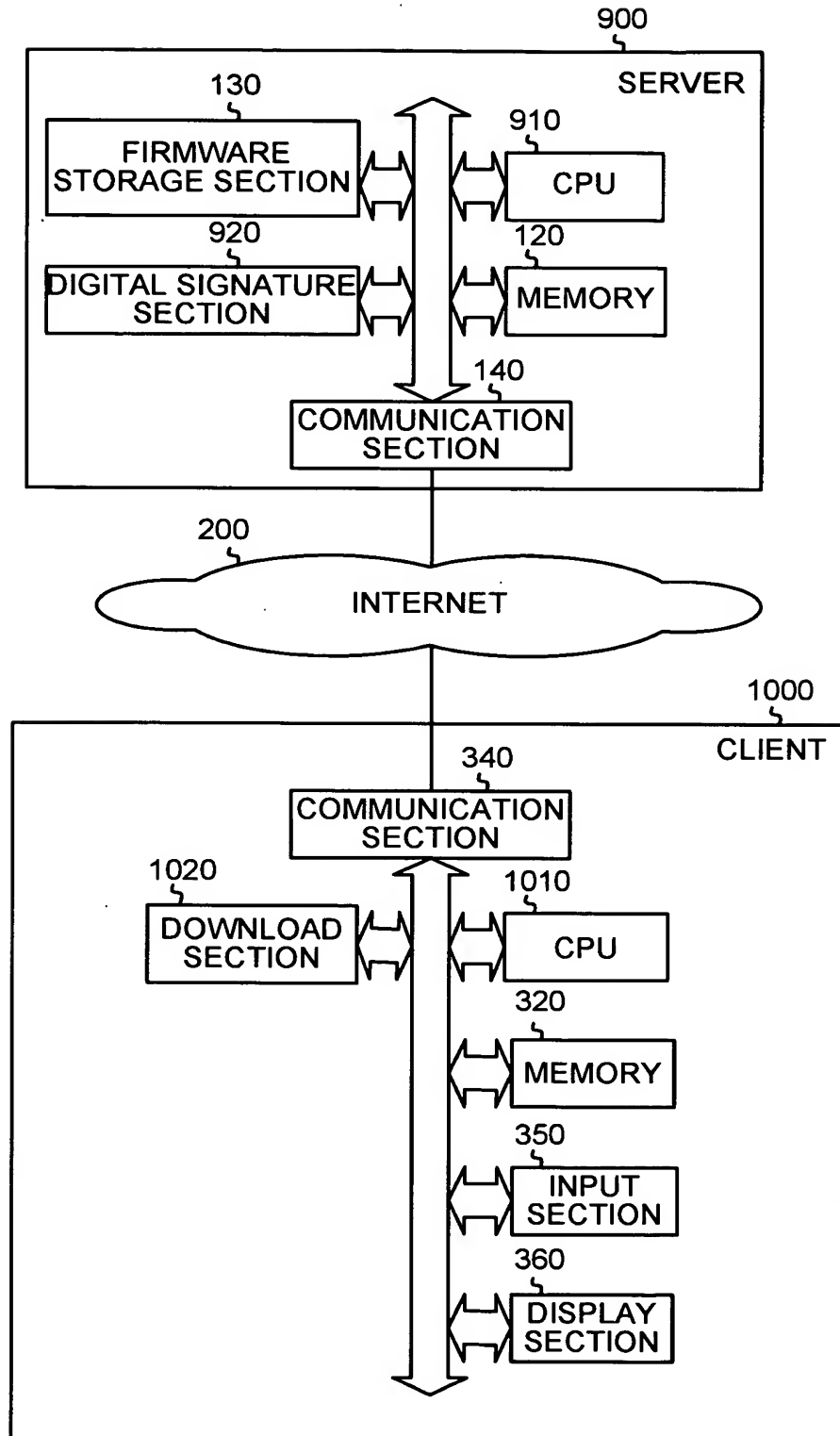


FIG.22

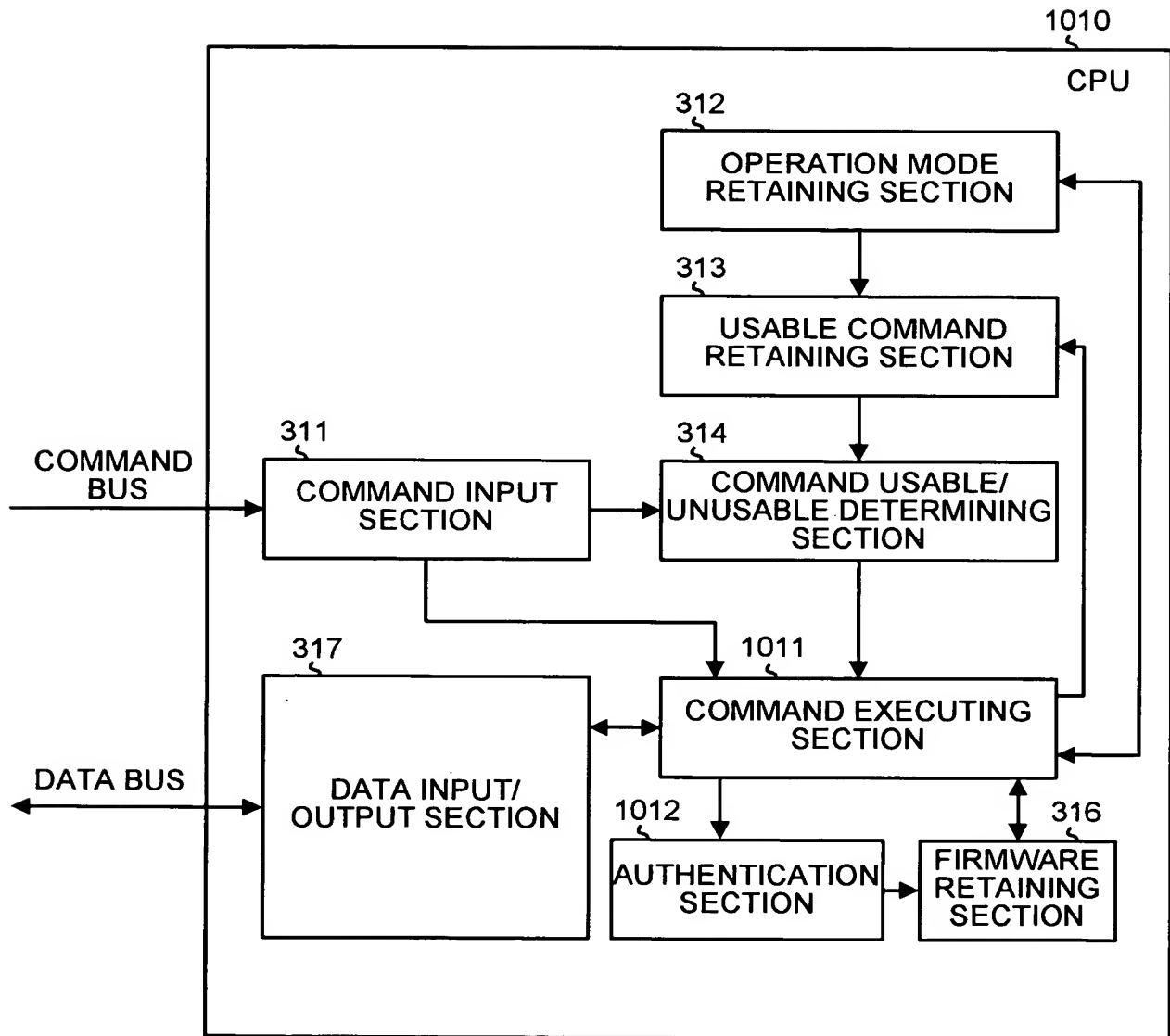


FIG.23

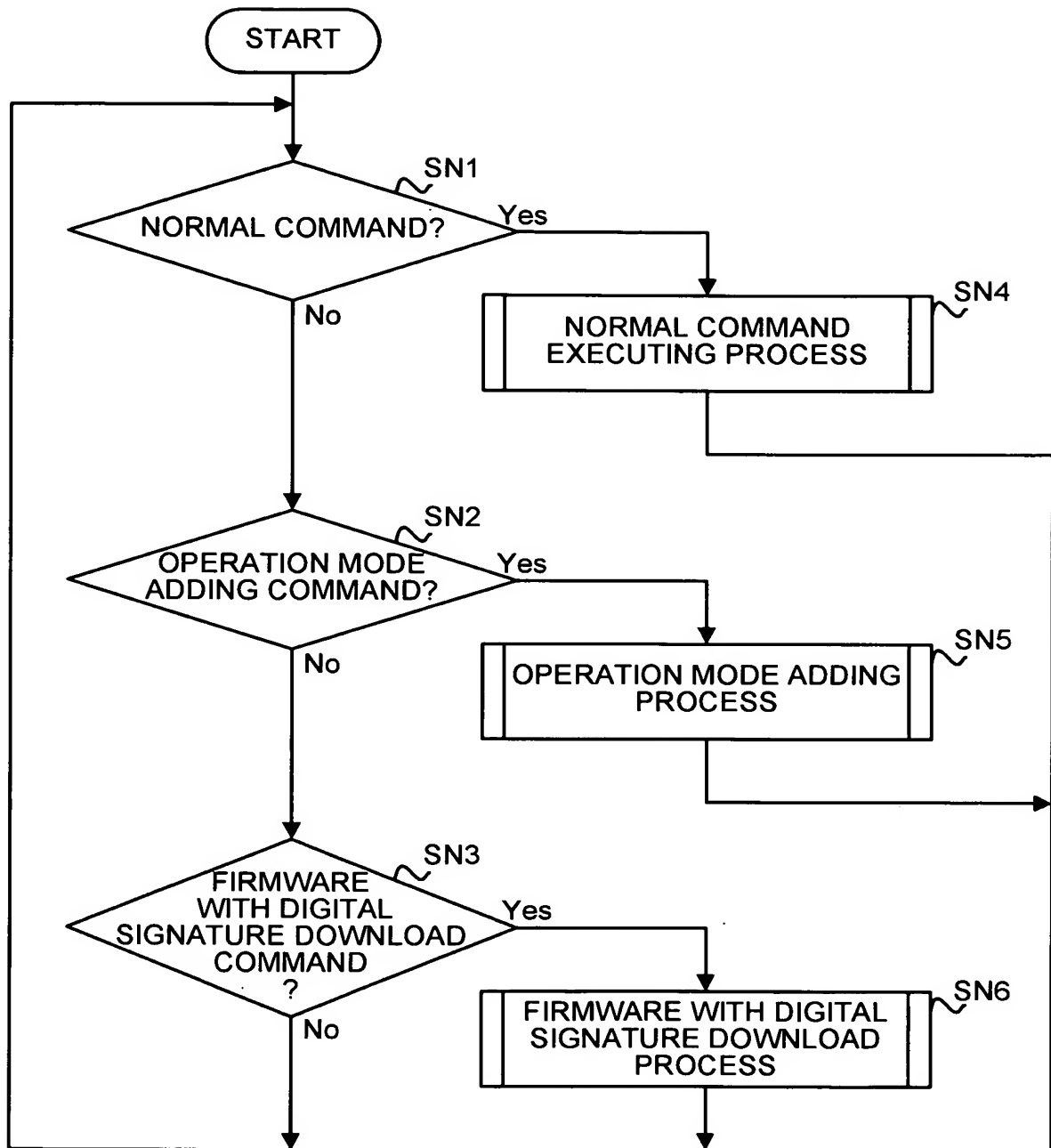


FIG.24

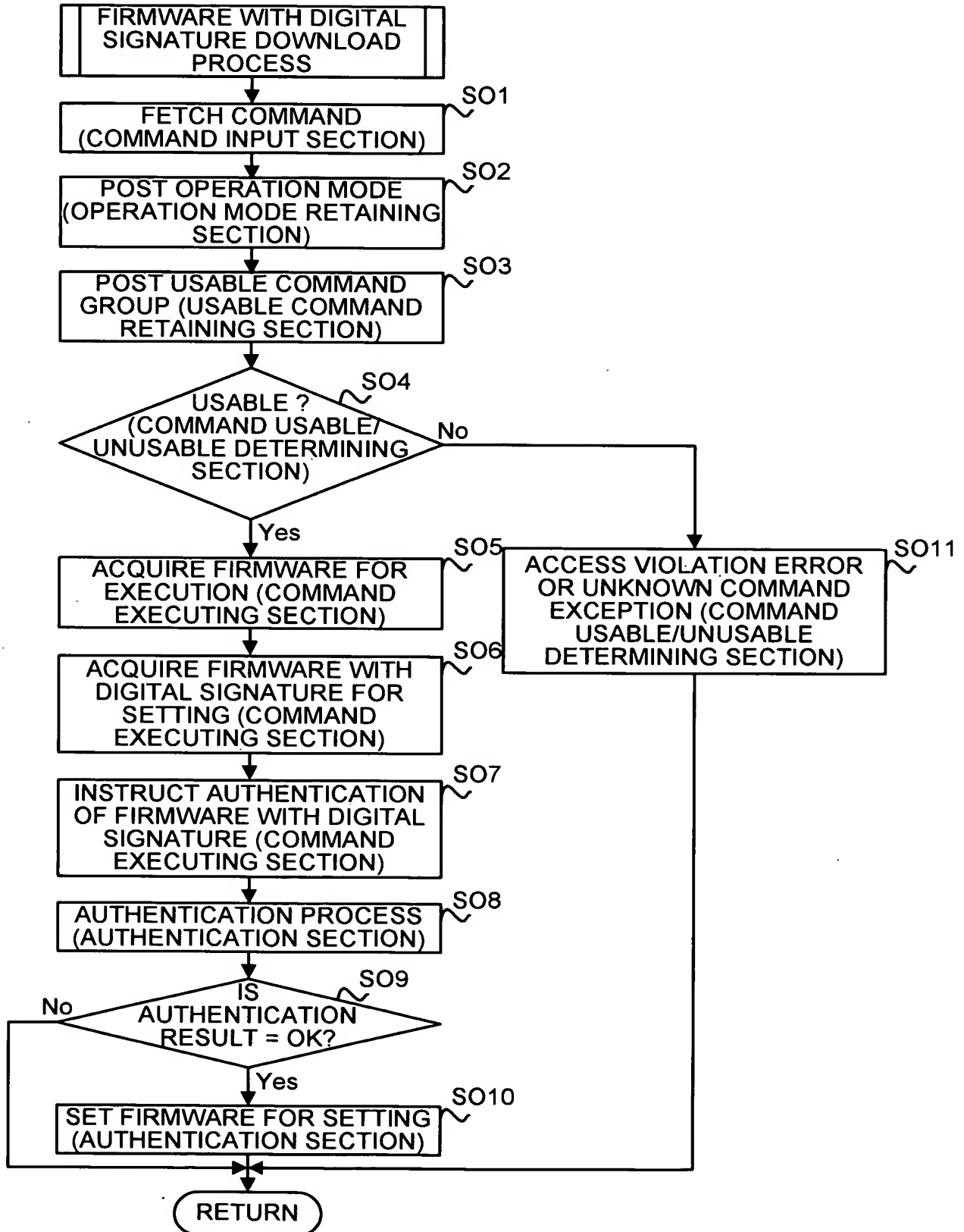


FIG.25

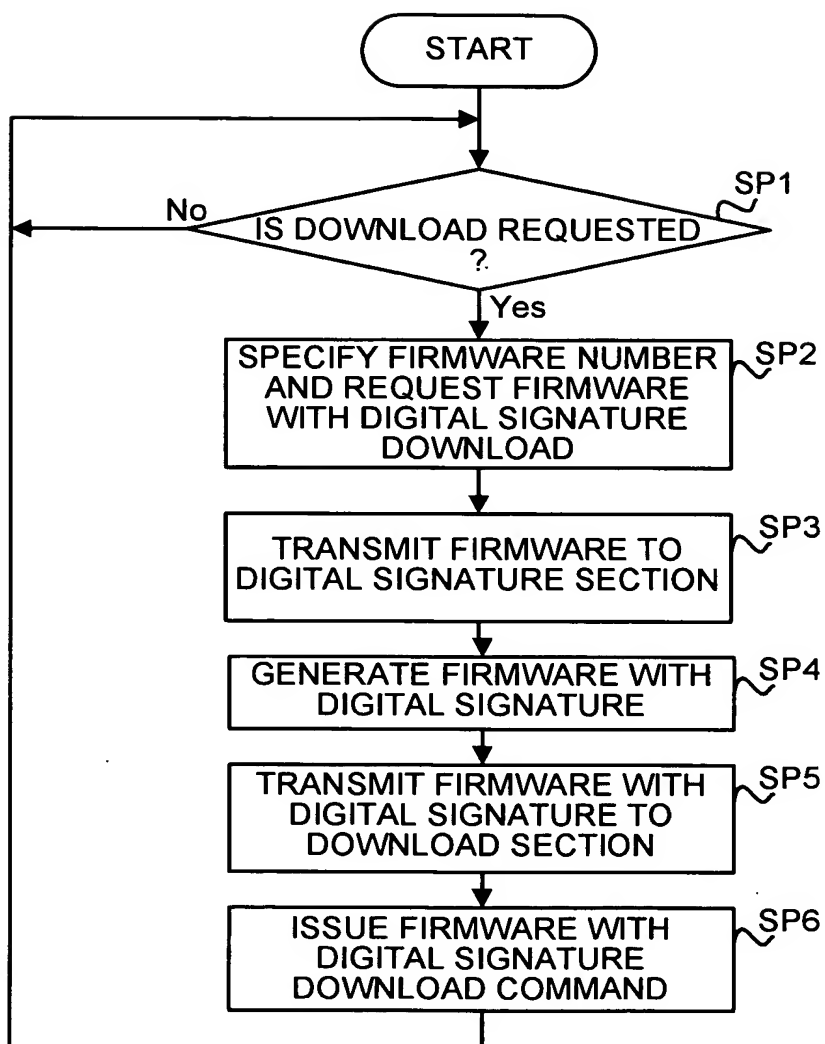


FIG.26

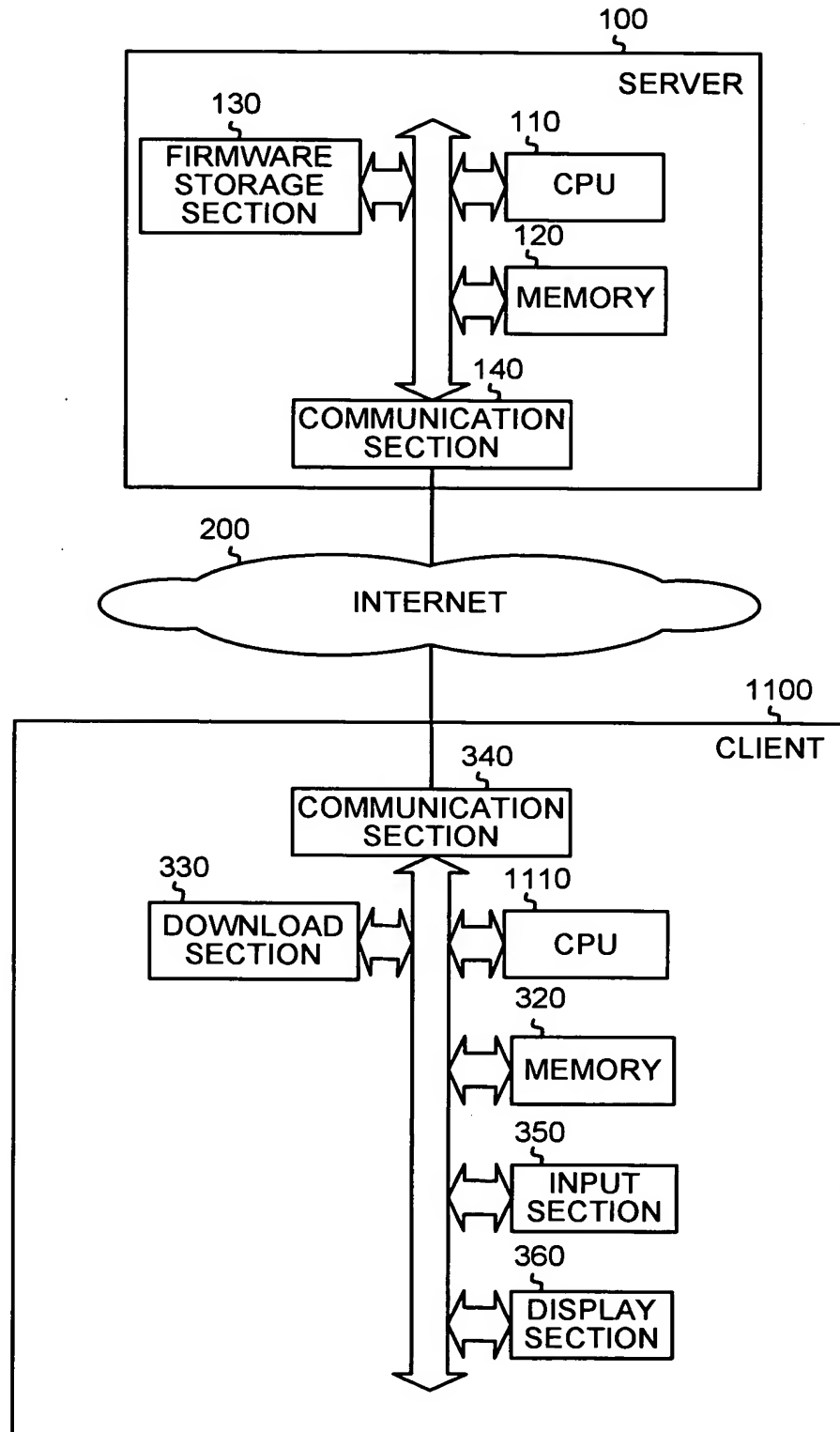


FIG.27

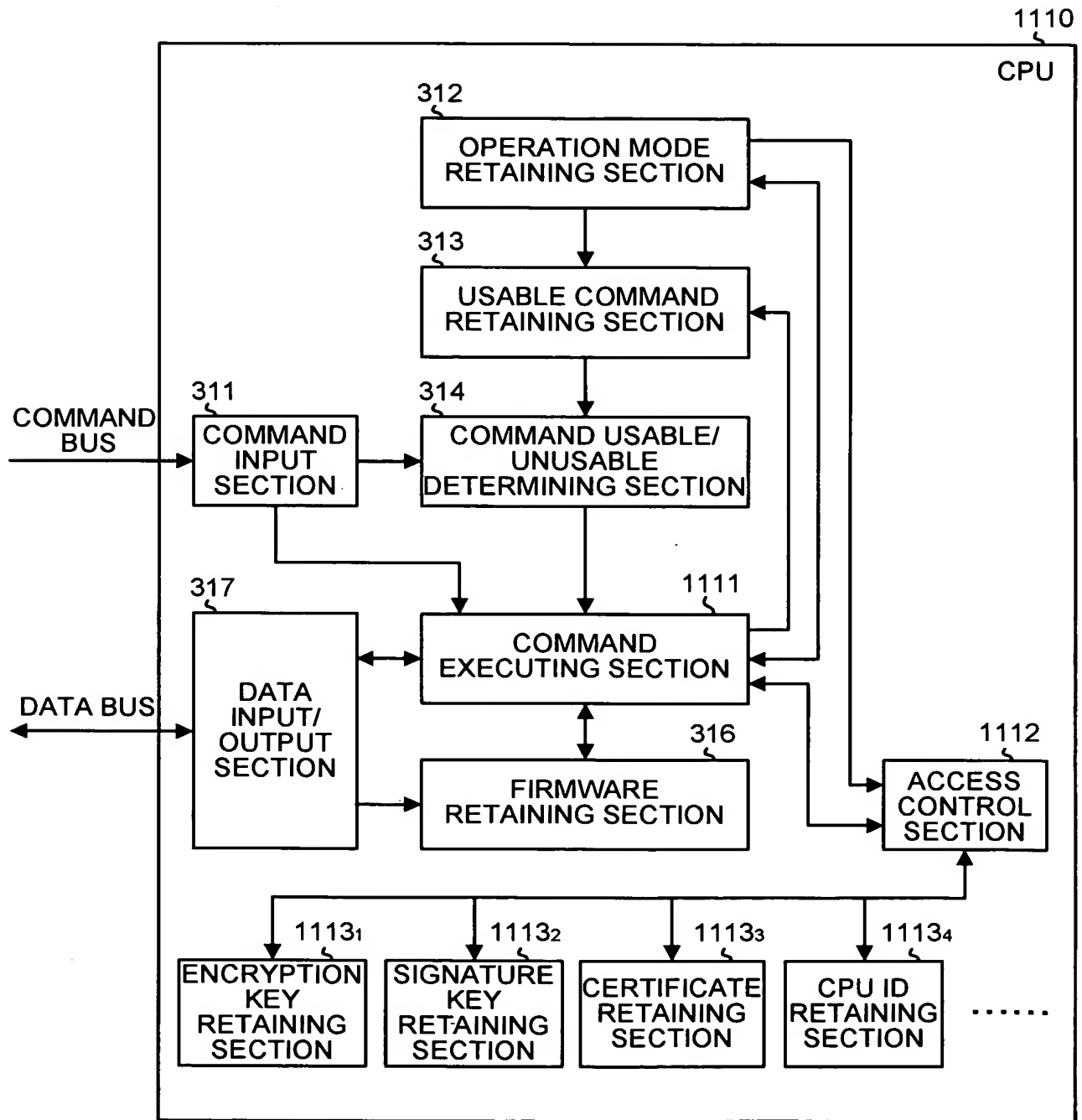


FIG.28

400
Σ

OPERATION MODE	ACCESSIBLE NUMBER	RESOURCE 1	RESOURCE 2	RESOURCE i	RESOURCE n
0	n	ENCRYPTION KEY	SIGNATURE KEY	CERTIFICATE	CPU ID
1	i	ENCRYPTION KEY	SIGNATURE KEY	CPU ID	—	—
.
.
k	1	SIGNATURE KEY	—	—	—	—	—

FIG.29

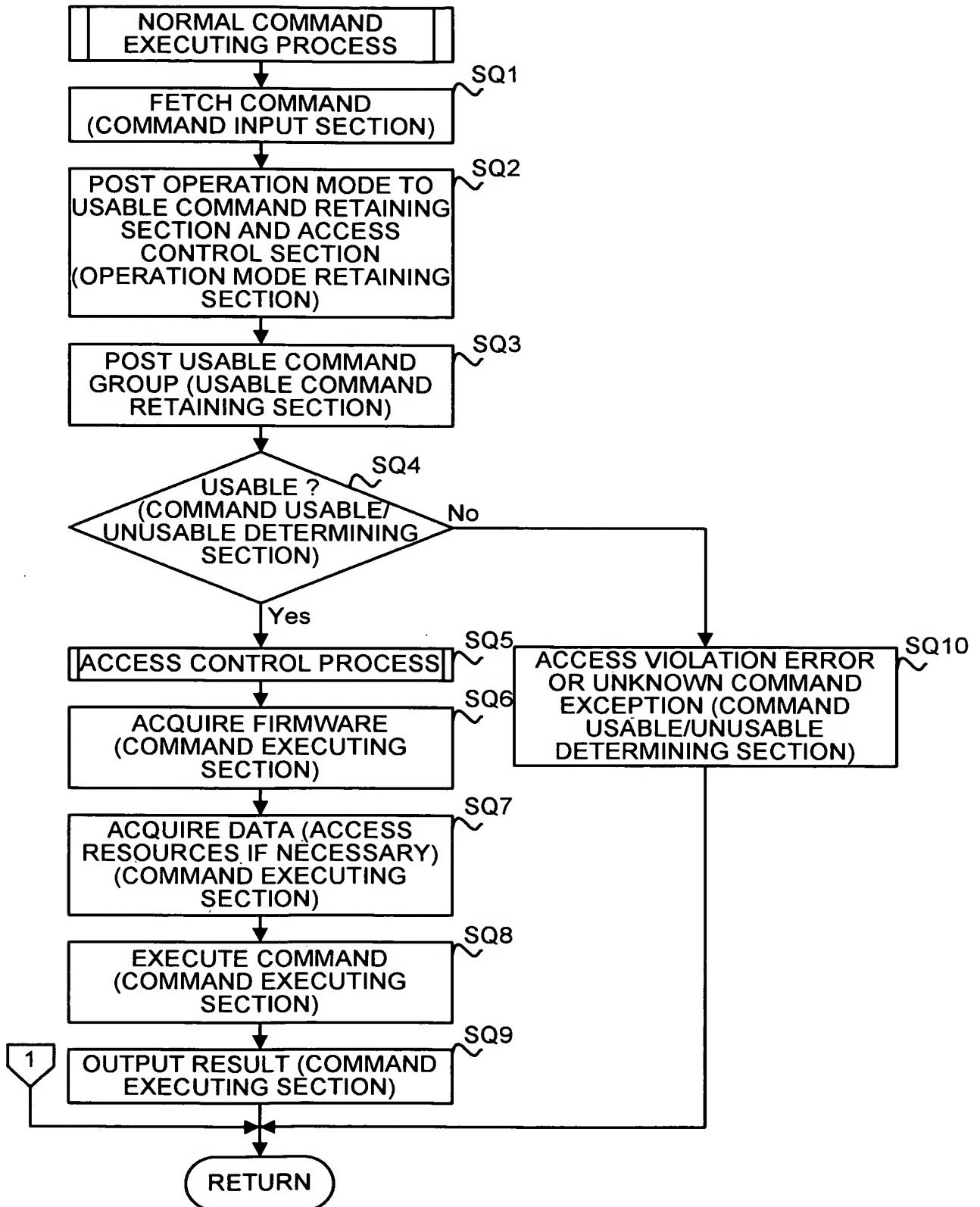


FIG.30

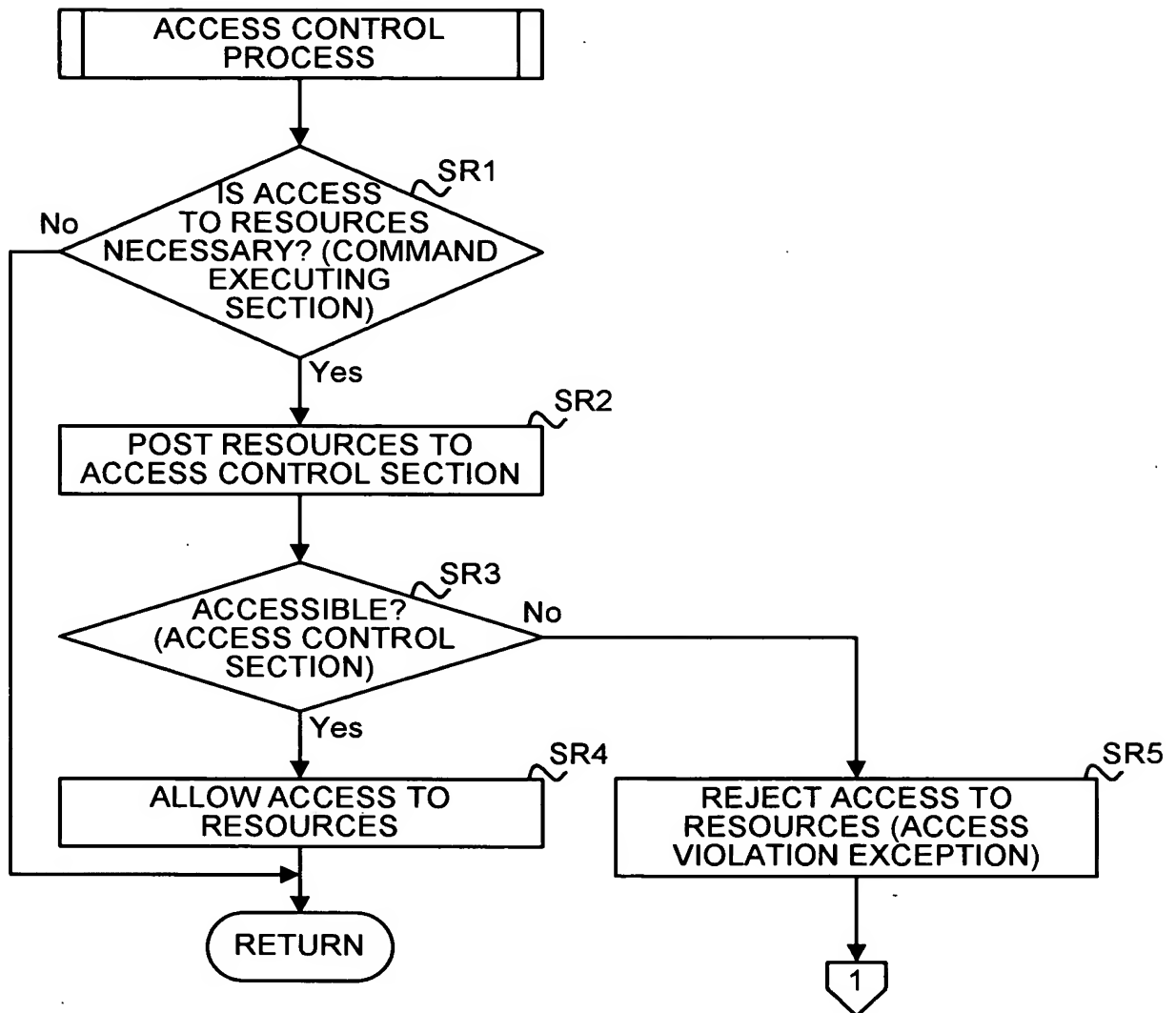


FIG.31

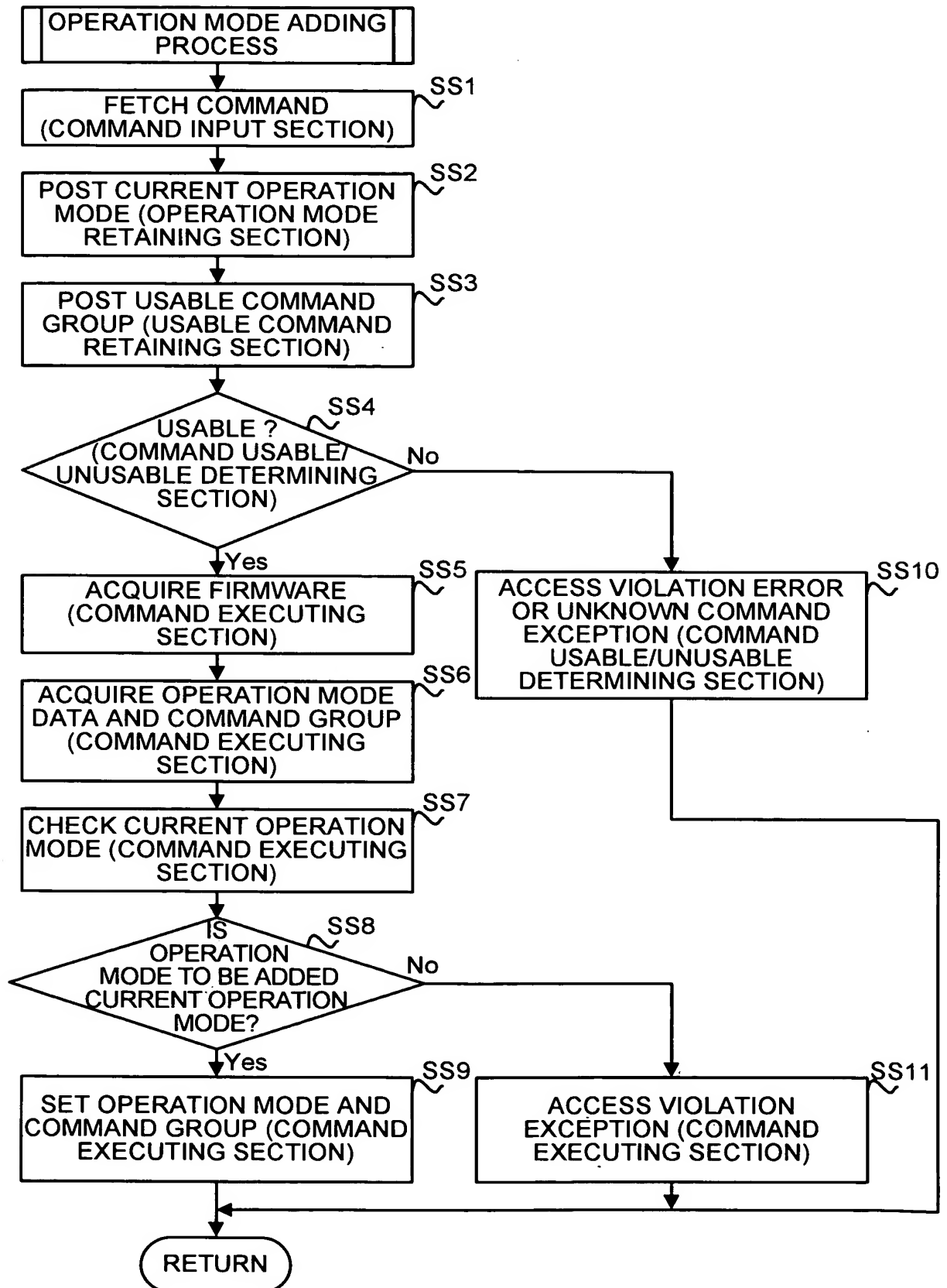


FIG.32

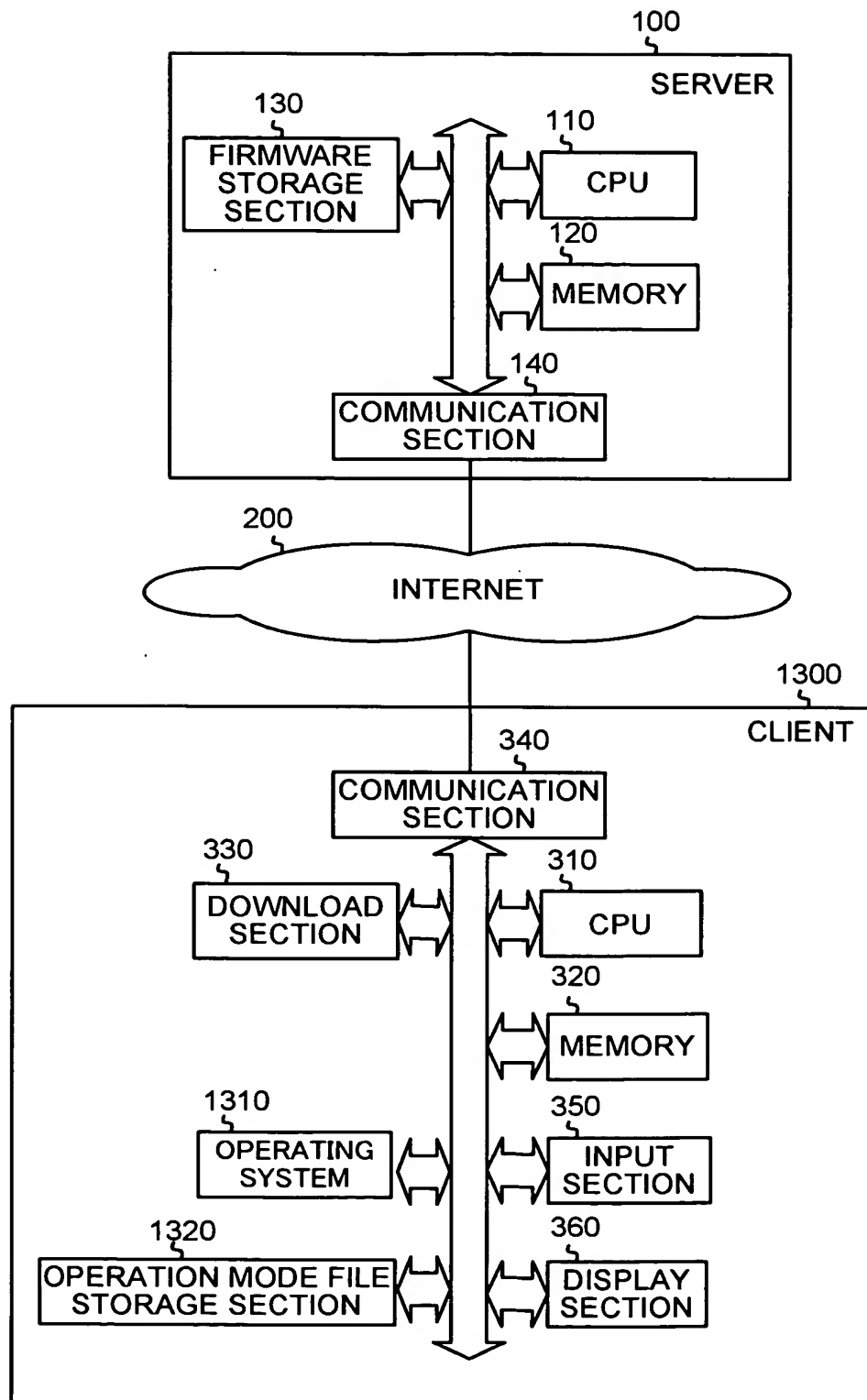
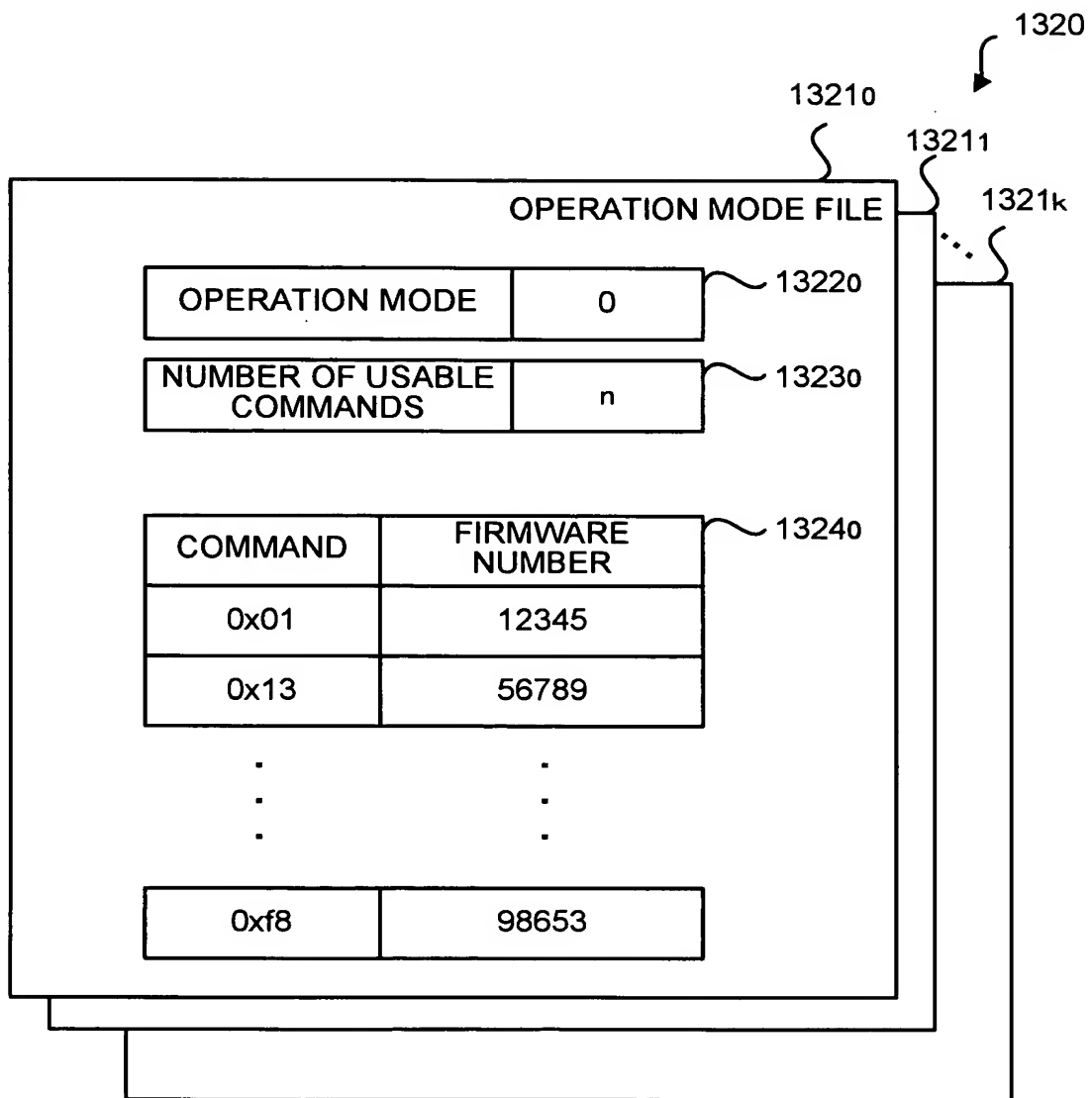


FIG.33



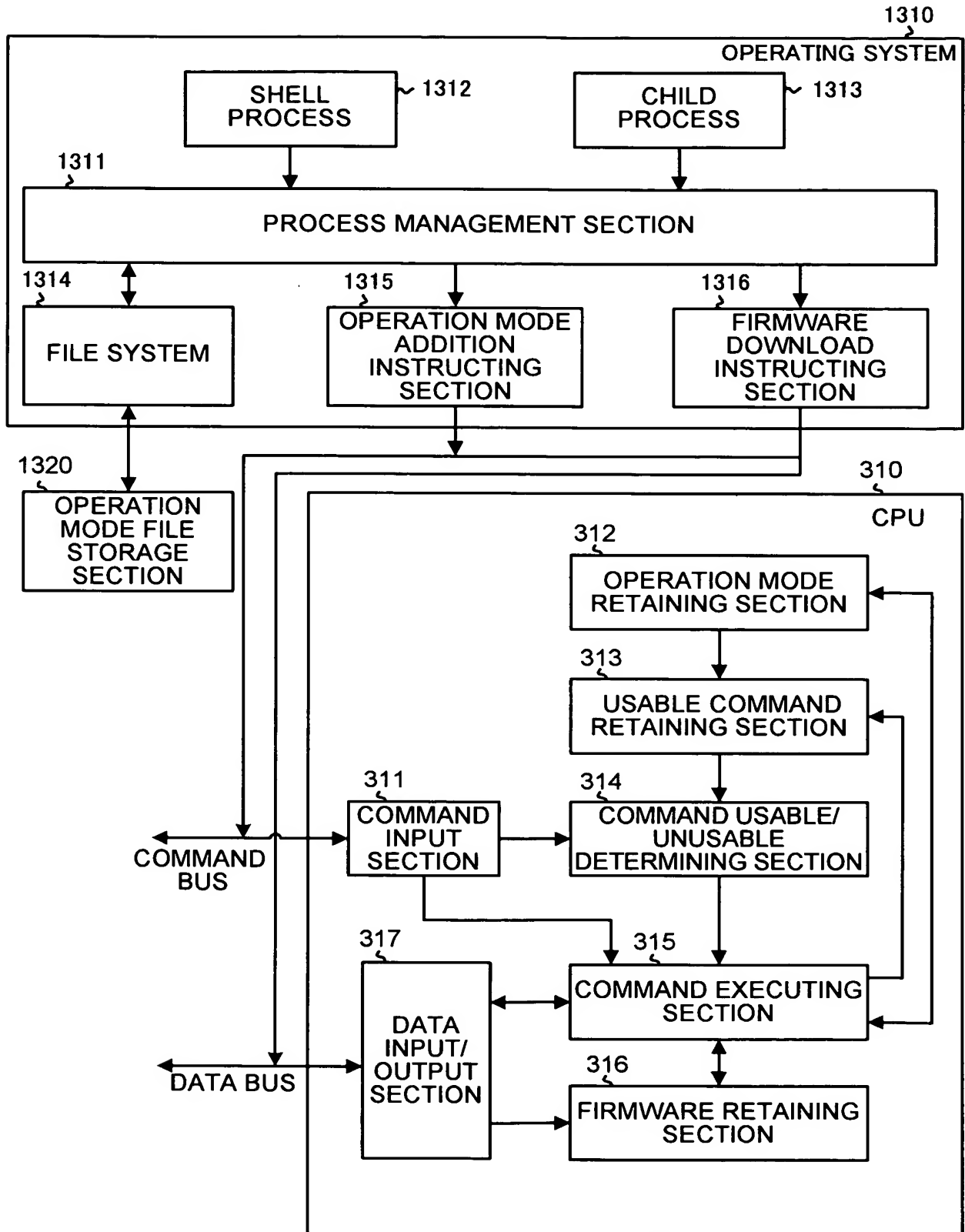


FIG.35

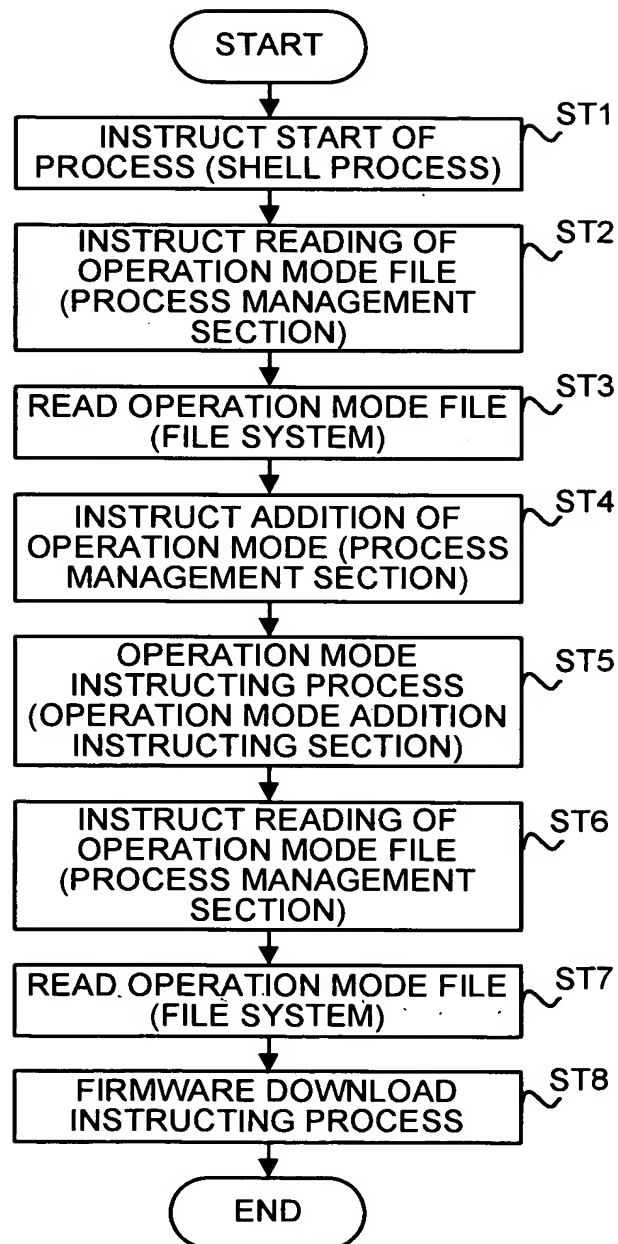


FIG.36

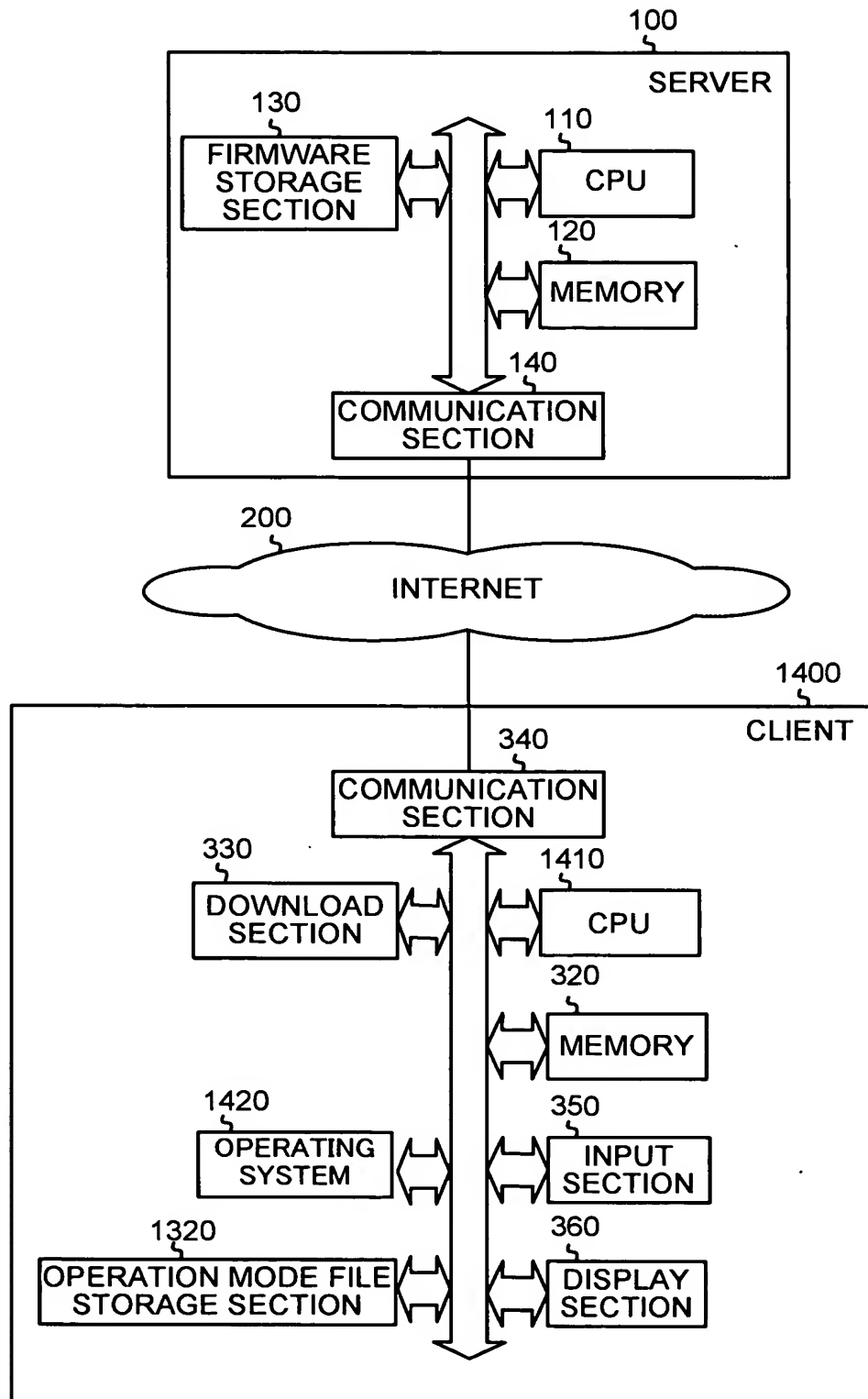


FIG.37

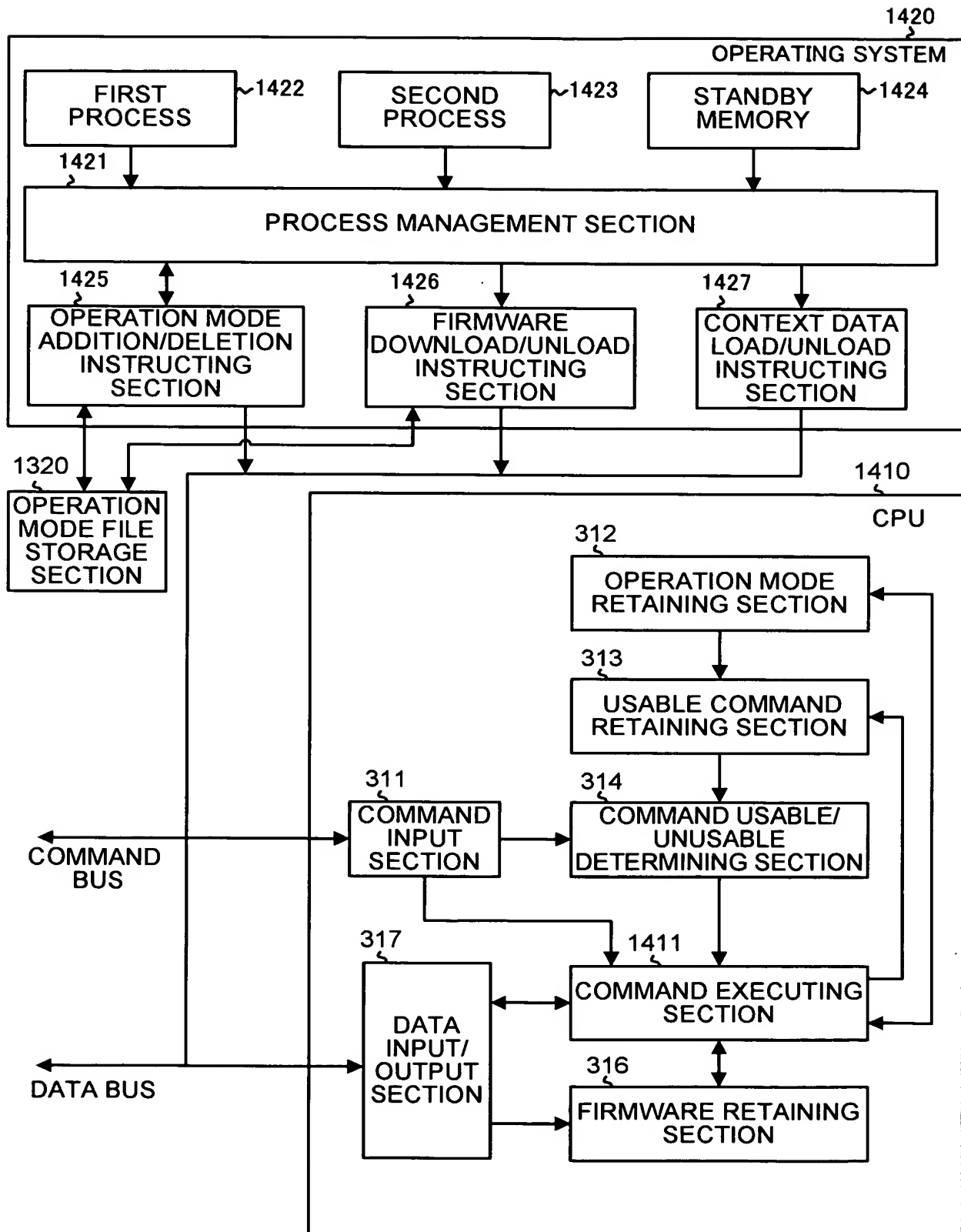


FIG.38

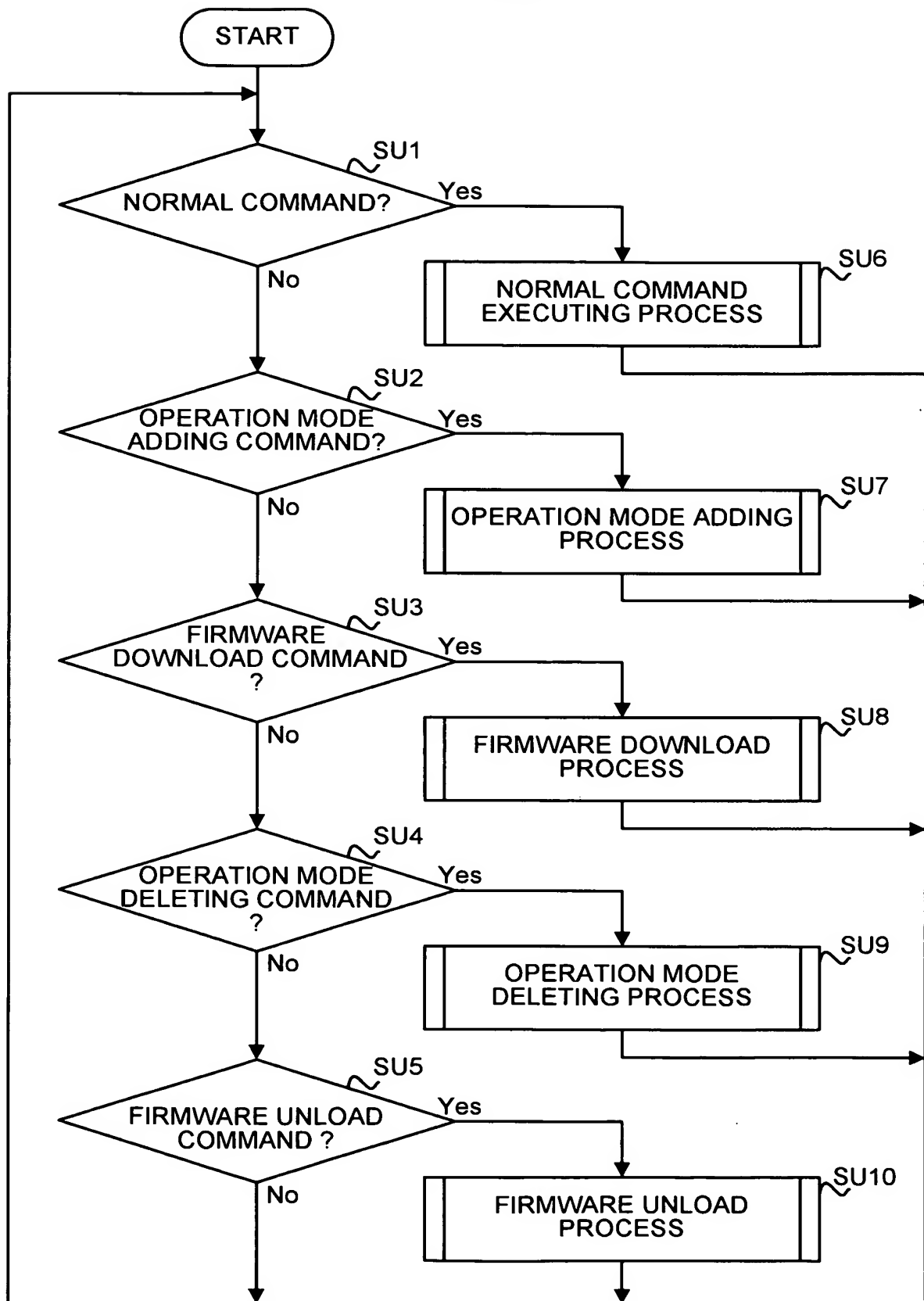


FIG.39

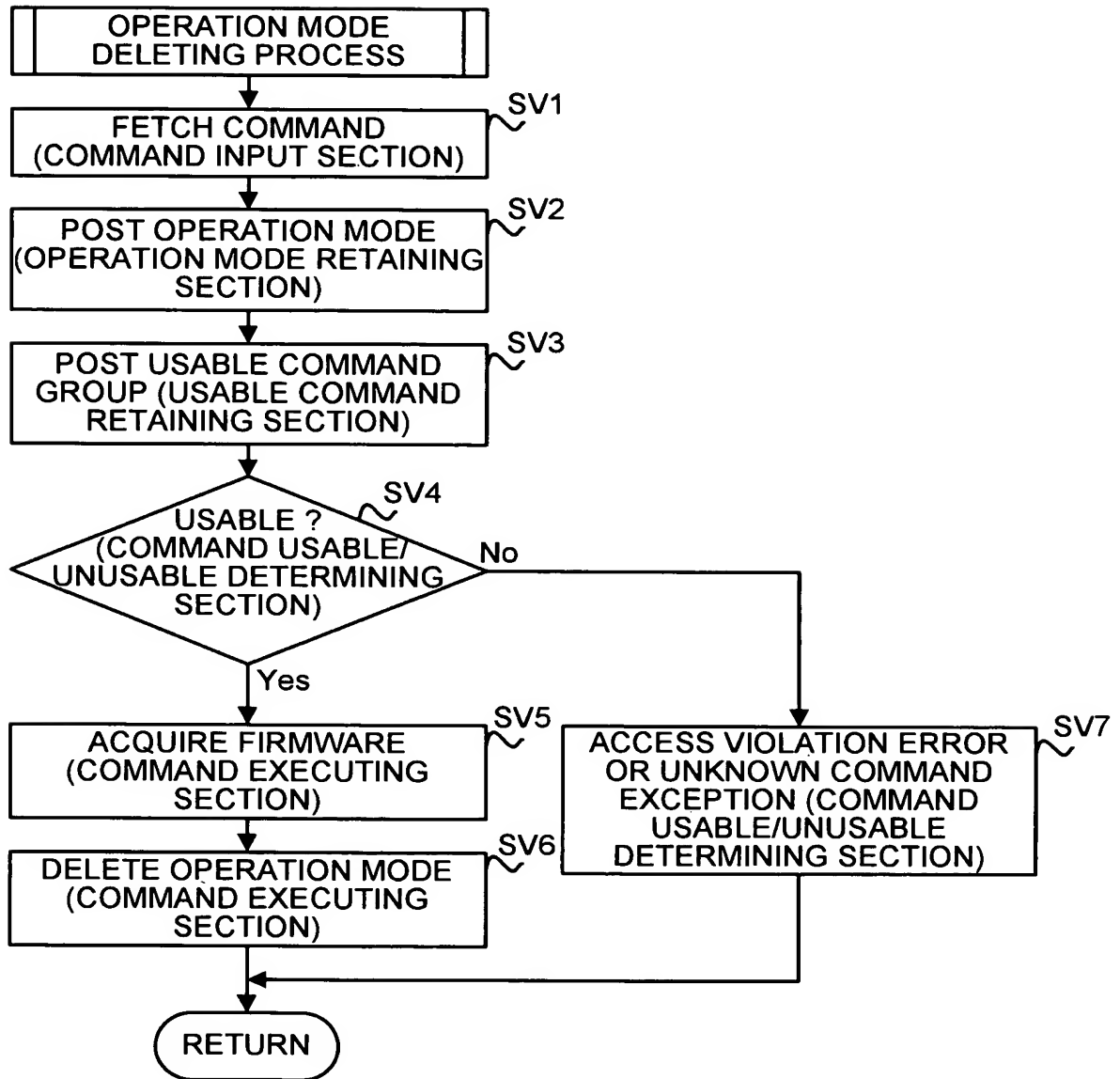


FIG.40

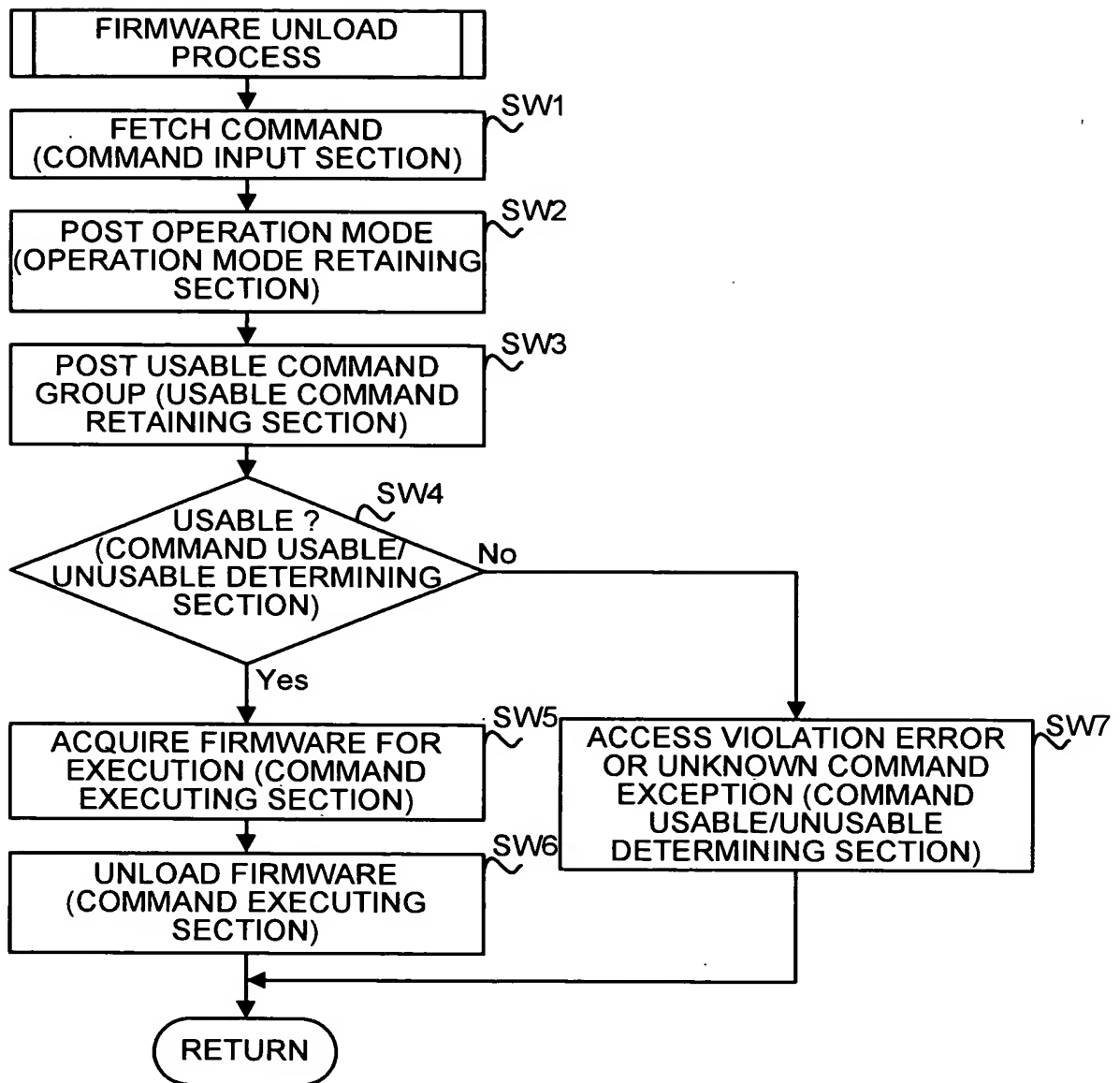


FIG.41

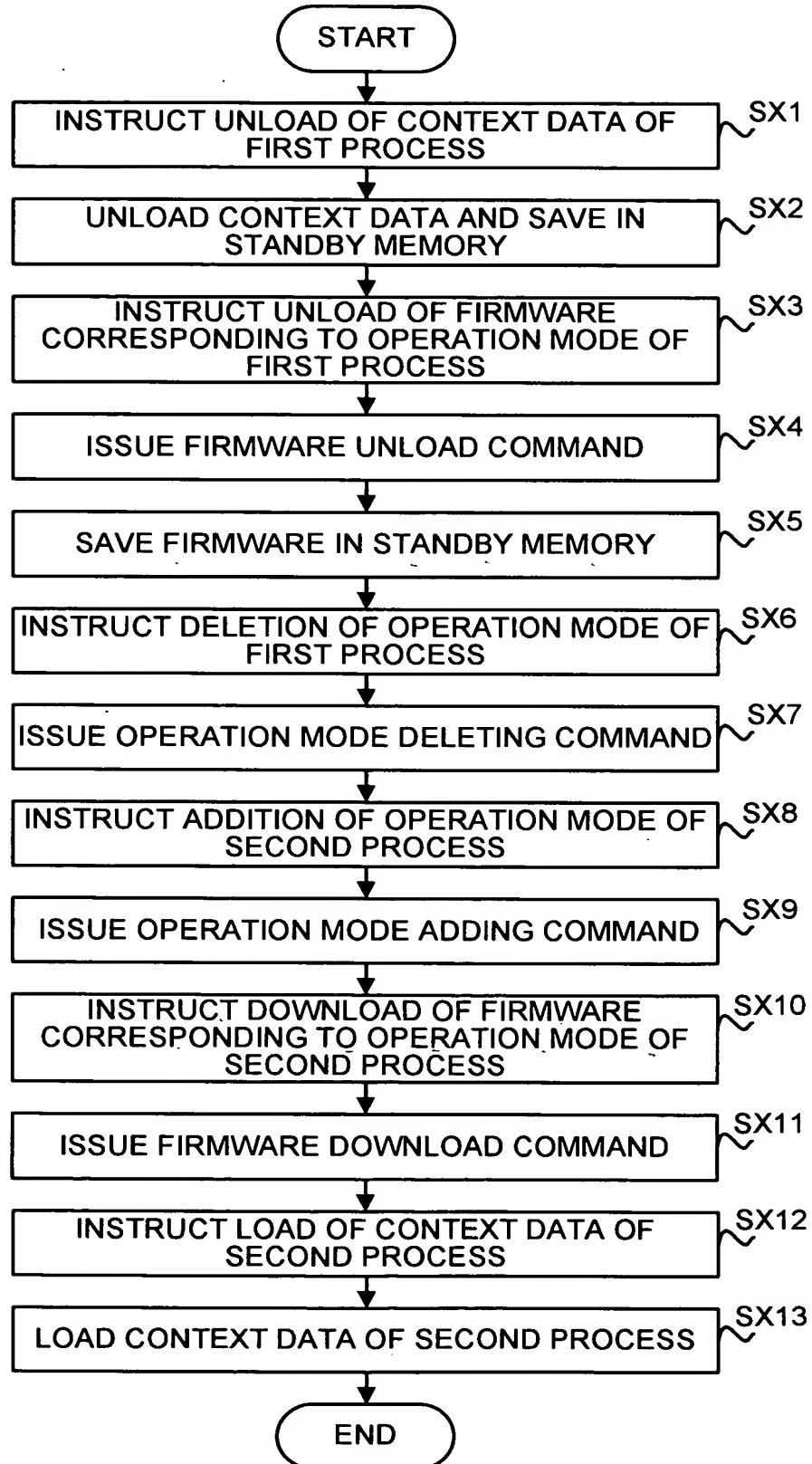


FIG.42

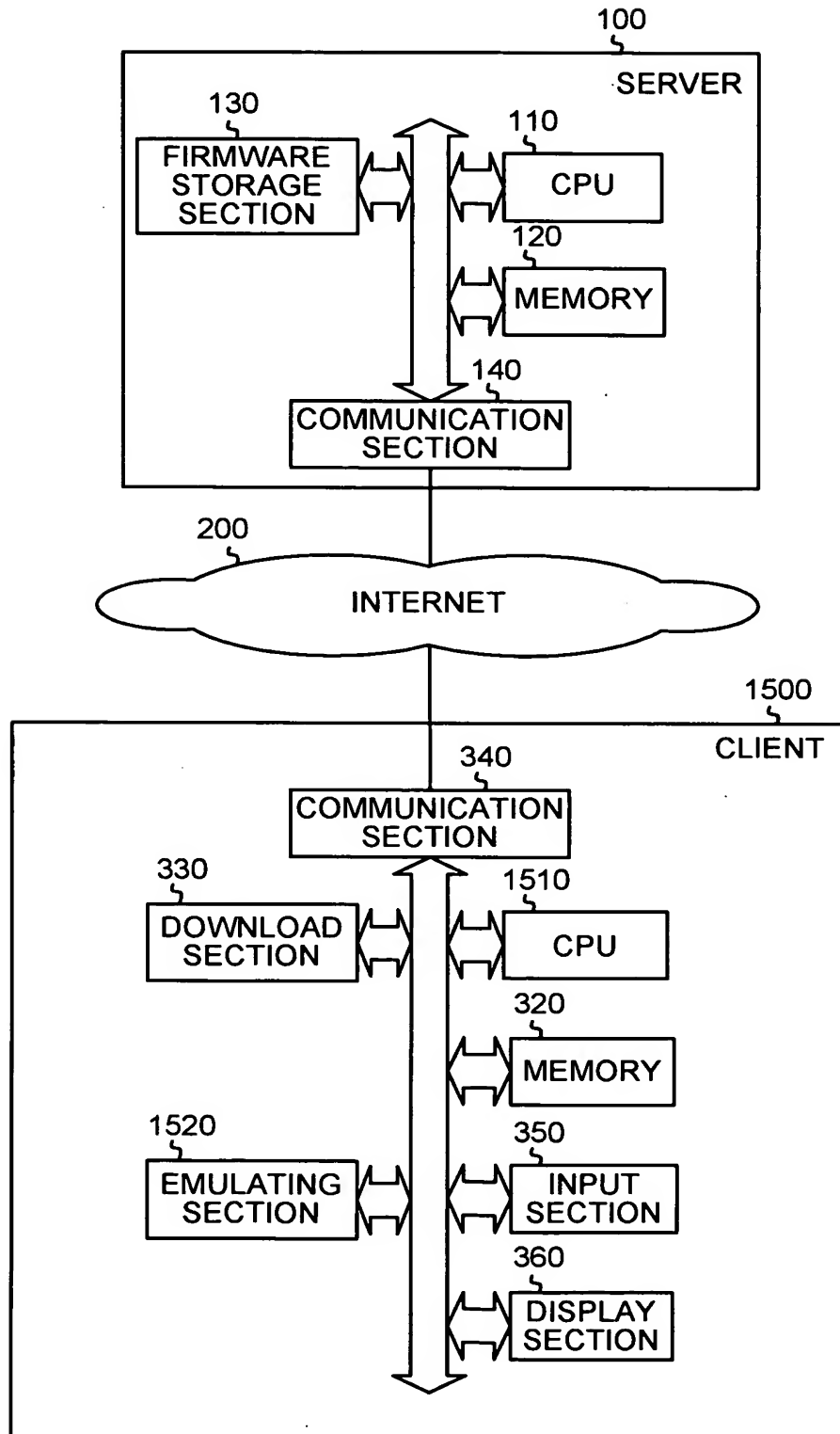


FIG.43

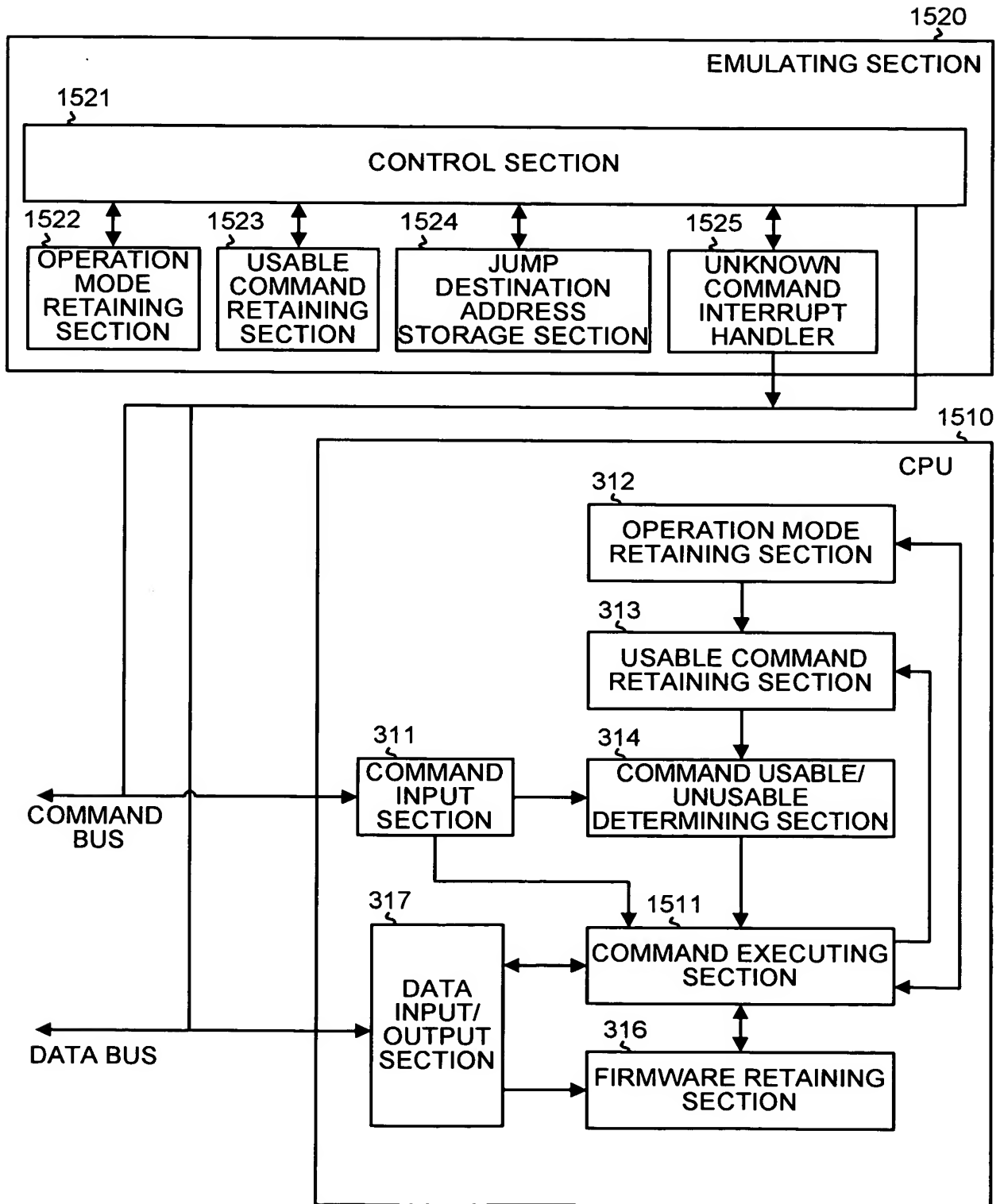


FIG.44

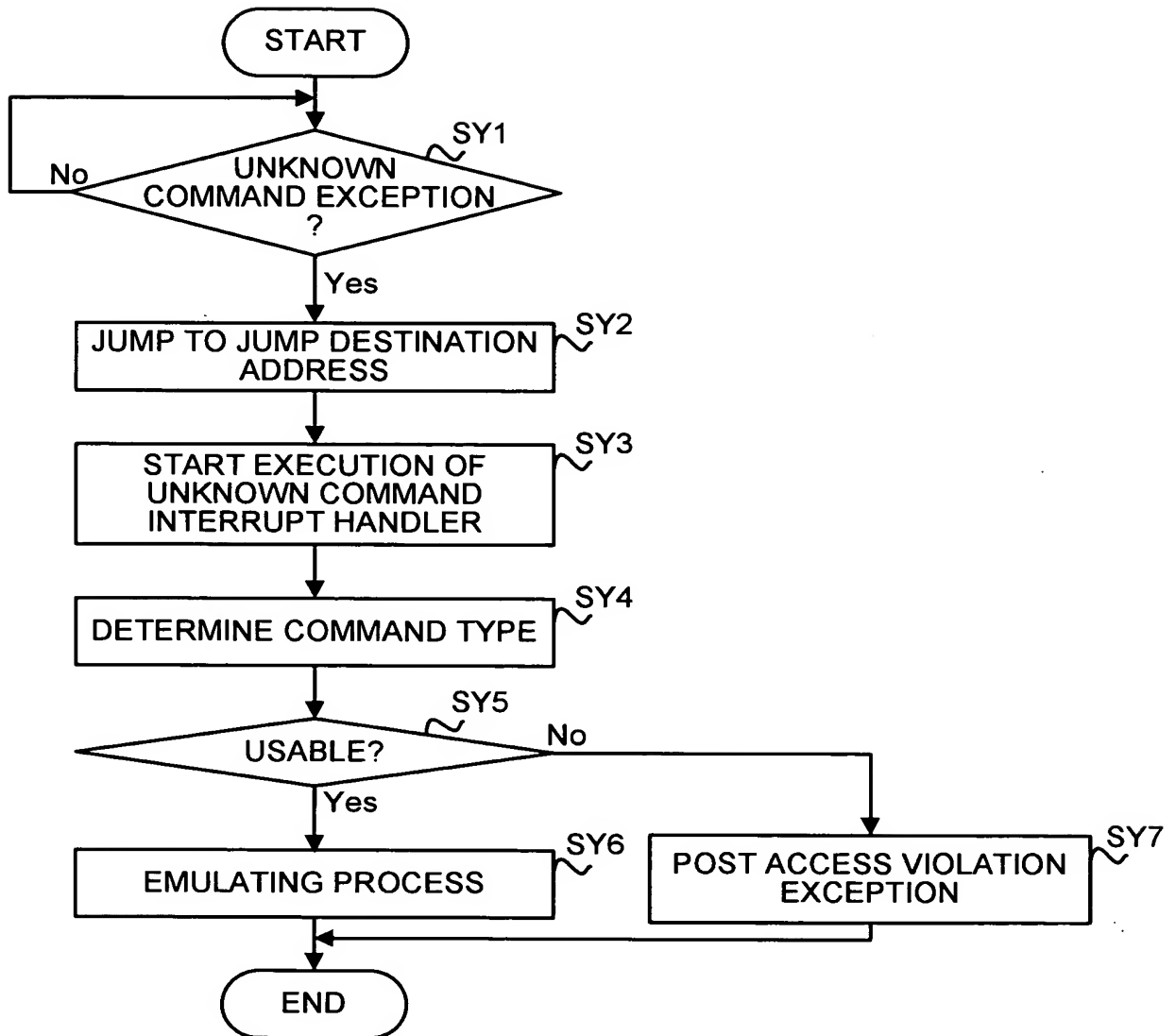


FIG.45

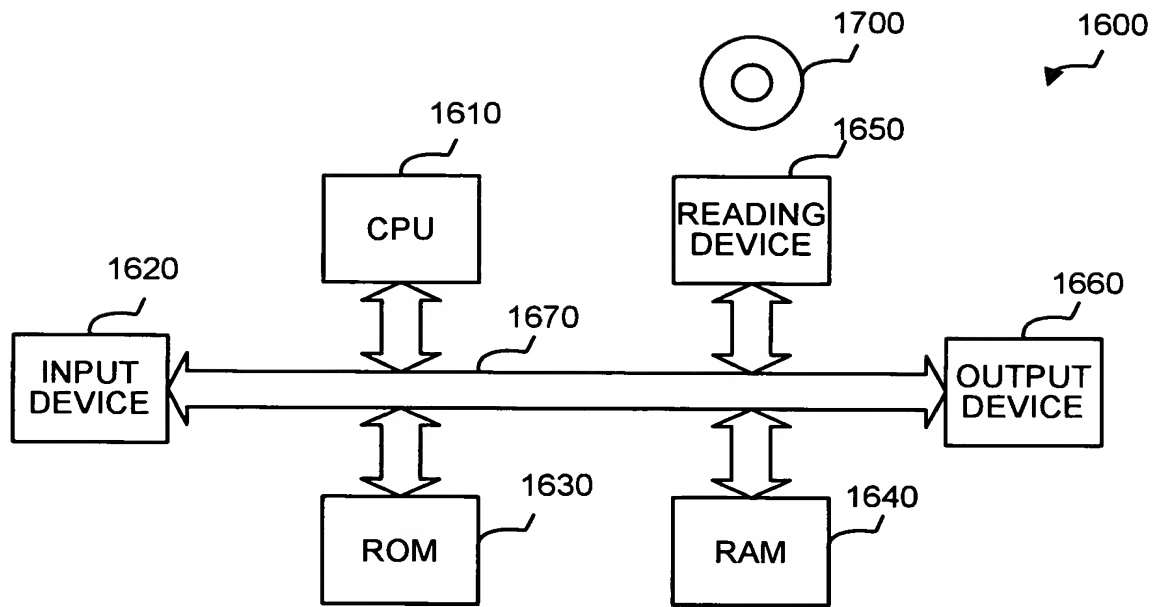


FIG.46

